

BANK 61

1E80h	Core registers	1EAAh	Unimplemented Read as '0'
1E8Bh			
1E8Ch	ANSELA		
1E8Dh	WPUA		
1E8Eh	ODCONA		
1E8Fh	SLRCONA		
1E90h	INLVLA		
1E91h	IOCAP		
1E92h	IOCAN		
1E93h	IOCAF		
1E94h	—		
1E95h	—		
1E96h	ANSELB		
1E97h	WPUB		
1E98h	ODCONB		
1E99h	SLRCONB		
1E9Ah	INLVLB		
1E9Bh	IOCBP	1EE4h	
1E9Ch	IOCBN	1EE5h	RB4I2C
1E9Dh	IOCBF	1EE6h	RB5I2C
1E9Eh	—	1EE7h	RB6I2C
1E9Fh	—	1EE8h	RB7I2C
1EA0h	ANSELC	1EE9h	RC0I2C
1EA1h	WPUC	1EEAh	RC1I2C
1EA2h	ODCONC	1EEBh	—
1EA3h	SLRCONC	1EECh	RC4I2C
1EA4h	INLVLC	1EEDh	RC5I2C
1EA5h	IOCCP	1EEEh	—
1EA6h	IOCCN	1EEFh	—
1EA7h	IOCCF	1EF0h	Common RAM (Accesses 70h-7Fh)
1EA8h	—	1EFFh	
1EA9h	—		

Note: 1. PIC16F18076 only.

Legend:



Unimplemented data memory locations, read as '0'