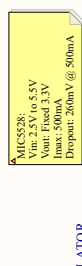
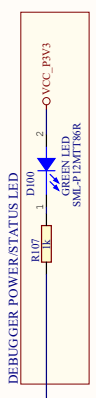


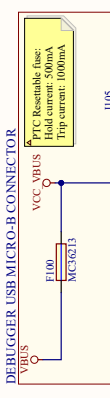
Interface Signal	ICSP TARGET	UPDI TARGET	SWD TARGET
CDC TX	UART RX	UART RX	UART RX
CDC RX	UART TX	UART TX	UART TX
DBG0	DAT	UPDI	SWDAT
DBG1	CLK	GPIO	SWCLK
DBG2	GPIO	SWO/GPIO	
DBG3	MCLR	RESET	RESET
VCC			



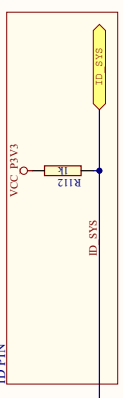
DEBUGGER REGULATOR



DEBUGGER POWER/STATUS LED



DEBUGGER USB MICRO-B CONNECTOR

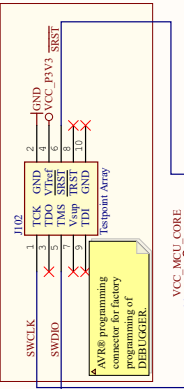


ID PIN

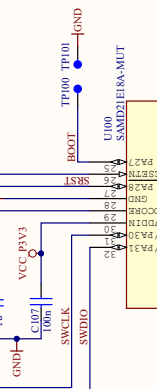
J105: Cut-strip used for full separation of target power from the level shifters and on-board regulators.
 - For current measurements using an external power supply, this strip could be cut for more accurate measurements. Leakage back through the switch is in the micro ampere range.
J101: This is a footprint for a 1x2 100mil pitch pin-header that can be used for easy current measurement and voltage drop measurement across the bootstrap.
 - Cut the track between the holes, and mount a pin-header

MIC5353:
 Vin = 2.0 to 6.0V
 Vout = 0.5V to 5.1V
 Iin max = 500mA
 Iout max = 500mA
 Dropout (typical): 50mV @ 150mA, 160mV @ 500mA
 Accuracy: 2% initial
 Thermal shutdown and current limit
 Maximum output voltage is limited by the input voltage and the dropout voltage in the regulator.
 (Vmax = Vin - dropout)

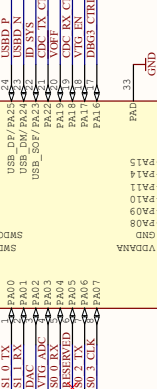
DEBUGGER TESTPOINT



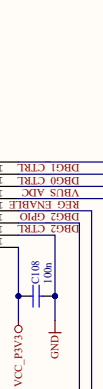
STATUS LED



VCC MCU CORE



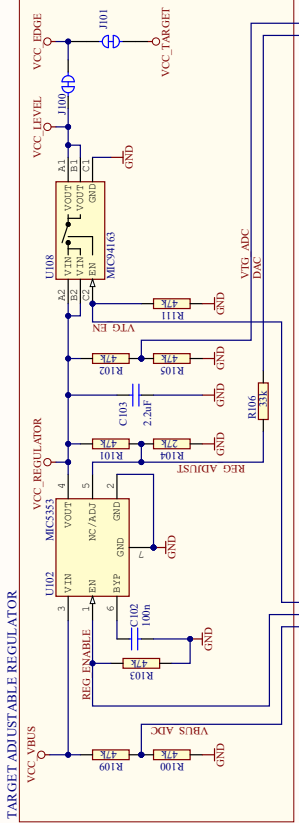
VCC_PV3



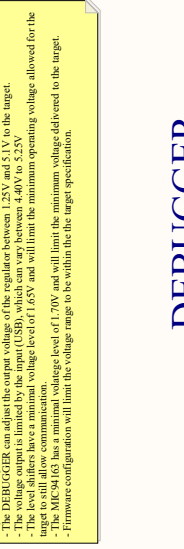
VCC_PV3

R113: Pull-down to prevent DBG3_CTRL from floating when debugger is not powered.

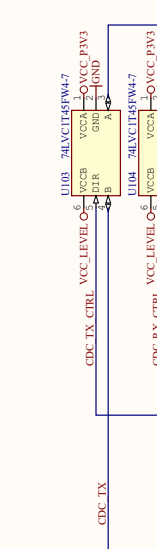
DEBUGGER



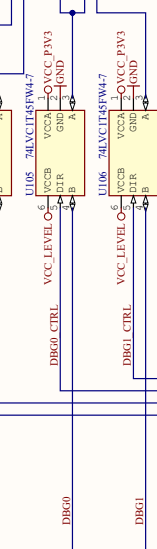
TARGET ADJUSTABLE REGULATOR



VCC LEVEL SHIFTERS



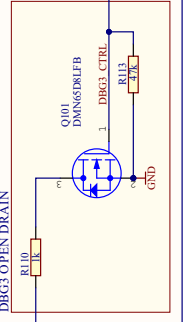
CDC TX



CDC RX



DBG0, DBG1, DBG2



DBG3 OPEN DRAIN

VDDIF

Drawn By: ST
 Equipped: TF

MICROCHIP

Project Title: **PIC16F15276 Curiosity Nano**

Sheet file: **Debugger**

Size: A3
 PCB Assembly Number: A09-3431
 PCB Number: A08-3132
 PCB Revision: 1

Designed with **Altium** Altium.com

File Path: I:\2020_Curiosity Nano Debugger\Source

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