

| Address       | Device                                       |                                    |
|---------------|--|------------------------------------|
|               | PIC18F24Q10                                  | PIC18F25Q10                        |
| <b>Note 1</b> | Stack (31 Levels)                            |                                    |
| 00 0000h      | Reset Vector                                 |                                    |
| ...           | ...  |                                    |
| 00 0008h      | Interrupt Vector High                        |                                    |
| ...           | ...  |                                    |
| 00 0018h      | Interrupt Vector Low                         |                                    |
| ...           | ...  |                                    |
| 00 001Ah      | Program Flash<br>Memory<br>(8 KW)            | Program Flash<br>Memory<br>(16 KW) |
| 00 3FFFh      |  |                                    |
| 00 4000h      |  |                                    |
| 00 7FFFh      |  |                                    |
| 00 8000h      |  | Not<br>Present <sup>(2)</sup>      |
| 00 FFFFh      |  |                                    |
| 01 0000h      |  |                                    |
| 01 FFFFh      |  |                                    |
| 02 0000h      |  |                                    |
| 1F FFFFh      |  |                                    |
| 20 0000h      | User IDs (128 Words) <sup>(3)</sup>          |                                    |
| 20 00FFh      |  |                                    |
| 20 0100h      | Reserved                                     |                                    |
| 2F FFFFh      |  |                                    |
| 30 0000h      | Configuration Words (6 Words) <sup>(3)</sup> |                                    |
| 30 000Bh      |  |                                    |
| 30 000Ch      | Reserved                                     |                                    |
| 30 02FFh      |  |                                    |
| 30 0300h      | Unimplemented                                |                                    |
| 30 FFFBh      |  |                                    |
| 31 0000h      | Data EEPROM (256 Bytes)                      |                                    |
| 31 00FFh      |  |                                    |
| 31 0100h      | Unimplemented                                |                                    |
| 3F FFFBh      |  |                                    |
| 3F FFFCh      | Revision ID (1 Word) <sup>(4)</sup>          |                                    |
| 3F FFFDh      |  |                                    |
| 3F FFFEh      | Device ID (1 Word) <sup>(4)</sup>            |                                    |
| 3F FFFFh      |  |                                    |

**Note 1:** The stack is a separate SRAM panel, apart from all user memory panels.

**2:** The addresses do not roll over. The region is read as '0'.

**3:** Not code-protected.

**4:** Device/Revision IDs are hard-coded in silicon.