

Bank	BSR[3:0] addr[11:8]	addr[7:0]	PIC18F	
			24Q10	25Q10
0	0h	00h-5Fh		
		60h-FFh		
1	1h	00h-FFh		
2	2h	00h-FFh		
3	3h	00h-FFh		
4	4h	00h-FFh		
5	5h	00h-FFh		
6	6h	00h-FFh		
7	7h	00h-FFh		
8	8h	00h-FFh		
9	9h	00h-FFh		
10	Ah	00h-FFh		
11	Bh	00h-FFh		
12	Ch	00h-FFh		
13	Dh	00h-FFh		
14	Eh	00h-9Ah		
		9Bh-FFh		
15	Fh	00h-5Fh		
		60h-FFh		

Virtual Access Bank

00h-5Fh

60h-FFh

GPR
SFR
Sector RAM
Unimplemented