Pin Module

Easy Setup | Registers

Selected Package : PDIP40

Pin Name	Module	Function	Custom Name	Start High	Analog	Output	WPU	OD	IOC
RA1	Pin Module	GPIO	SWITCH_S1	☐	☐	☐	☐	☐	none
RA2	Pin Module	GPIO	SWITCH_S2	☐	☐	☐	☐	☐	none
RA5	CWG3	CWG3B		☐	☐	☑	☐	☐	none
RB1	CWG1	CWG1B		☐	☐	☑	☐	☐	none
RB2	CWG1	CWG1A		☐	☐	☑	☐	☐	none
RB3	CMP1	C1IN2-		☐	☑	☐	☐	☐	none
RB4	CWG2	CWG2A		☐	☐	☑	☐	☐	none
RB6	PWM3	PWM3ERS		☐	☐	☐	☐	☐	none
RC2	PWM2	PWM2ERS		☐	☐	☐	☐	☐	none
RC2	PWM3	PWMIN0		☐	☐	☐	☐	☐	none
RC2	PWM1	PWM1ERS		☐	☐	☐	☐	☐	none
RC2	PWM2	PWMIN0		☐	☐	☐	☐	☐	none
RC2	PWM1	PWMIN0		☐	☐	☐	☐	☐	none
RC3	CCP1	CCP1		☐	☐	☑	☐	☐	none
RC4	Pin Module	GPIO	HALLA	☐	☐	☐	☐	☐	any
RC5	Pin Module	GPIO	HALLC	☐	☐	☐	☐	☐	any
RC6	Pin Module	GPIO	HALLB	☐	☐	☐	☐	☐	any
RC6	TMR2	T2IN	HALLB	☐	☐	☐	☐	☐	any
RC7	CWG3	CWG3A		☐	☐	☑	☐	☐	none
RD7	CWG2	CWG2B		☐	☐	☑	☐	☐	

Pin Manager: Grid View

Package: PDIP40		Pin No:	2	3	4	5	6	7	14	13	33	34	35	36	37	38	39	40	15	16	17	18	23	24	25	26	19	20	21	22	27	28	29	30	8	9	10	1
			Port A ▼								Port B ▼								Port C ▼								Port D ▼								Port E ▼			
Module	Function	Direction	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0	1	2	3
CCP1	CCP1	output																																				
CLC1	CLC1	output																																				
CLCx	-	-																																				
CMP1 ▼	C1IN0+	input																																				
	C1IN1+	input																																				
	C1INx-	input																																				
	C1OUT	output																																				
CWG1 ▼	CWG1	input																																				
	CWG1A	output																																				
	CWG1B	output																																				
	CWG1C	output																																				
	CWG1D	output																																				
CWG2 ▼	CWG2	input																																				
	CWG2A	output																																				
	CWG2B	output																																				
	CWG2C	output																																				
	CWG2D	output																																				
CWG3 ▼	CWG3	input																																				
	CWG3A	output																																				
	CWG3B	output																																				
	CWG3C	output																																				
	CWG3D	output																																				

			Port A ▼								Port B ▼								Port C ▼								Port D ▼								Port E ▼			
Module	Function	Direction	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0	1	2	3	4	5	6	7	0	1	2	3
Pin Module ▼	GPIO	input																																				
	GPIO	output																																				
RESET	MCLR	input																																				
TMR0 ▼	T0CKI	input																																				
	TMR0	output																																				
TMR1 ▼	T1CKI	input																																				
	T1G	input																																				
TMR2	T2IN	input																																				
TMR3 ▼	T3CKI	input																																				
	T3G	input																																				
TMR4	T4IN	input																																				