



The image displays a PCB layout with two data lanes. The top lane is highlighted in orange and the bottom lane in light blue. Both lanes consist of multiple parallel traces with complex, meandering patterns. The background is a light blue color with various circular patterns representing vias and pads. Two text boxes with leader lines provide specifications for each lane.

Data lane 0 (D0-D7)
Trace width = 5 mils
Trace clearance = 8 mils

Data lane 3 (D24-D31)
Trace width = 5 mils
Trace clearance = 9 mils