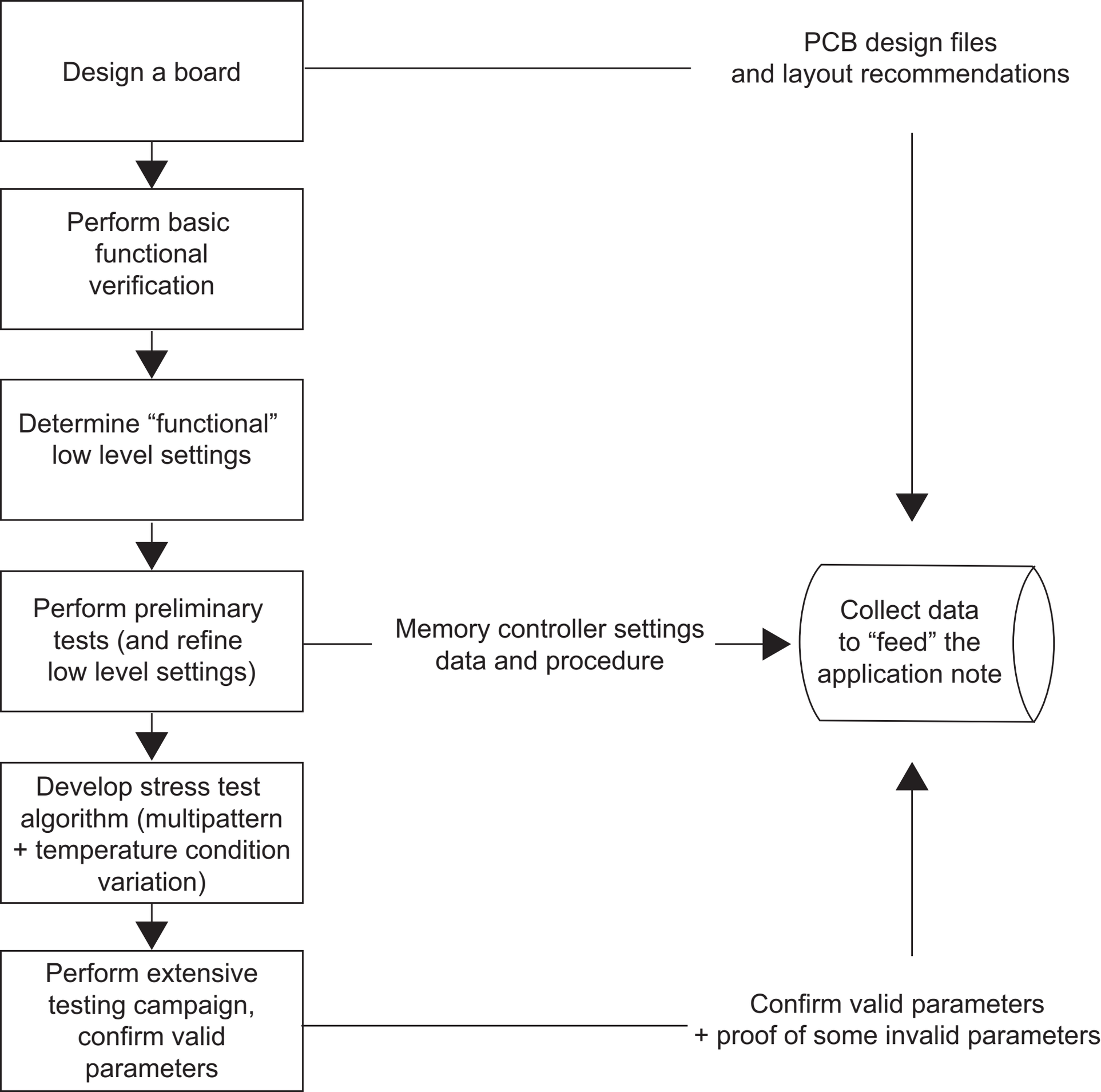
Design a board
PCB design files
and layout recommendations
Perform basic
functional
verification
Determine “functional”
low level settings
Perform preliminary
tests (and refine
low level settings)
Memory controller settings
data and procedure
Collect data
to “feed” the
application note
Develop stress test
algorithm (multipattern
+ temperature condition
variation)
Perform extensive
testing campaign,
confirm valid
parameters
Confirm valid parameters
+ proof of some invalid parameters