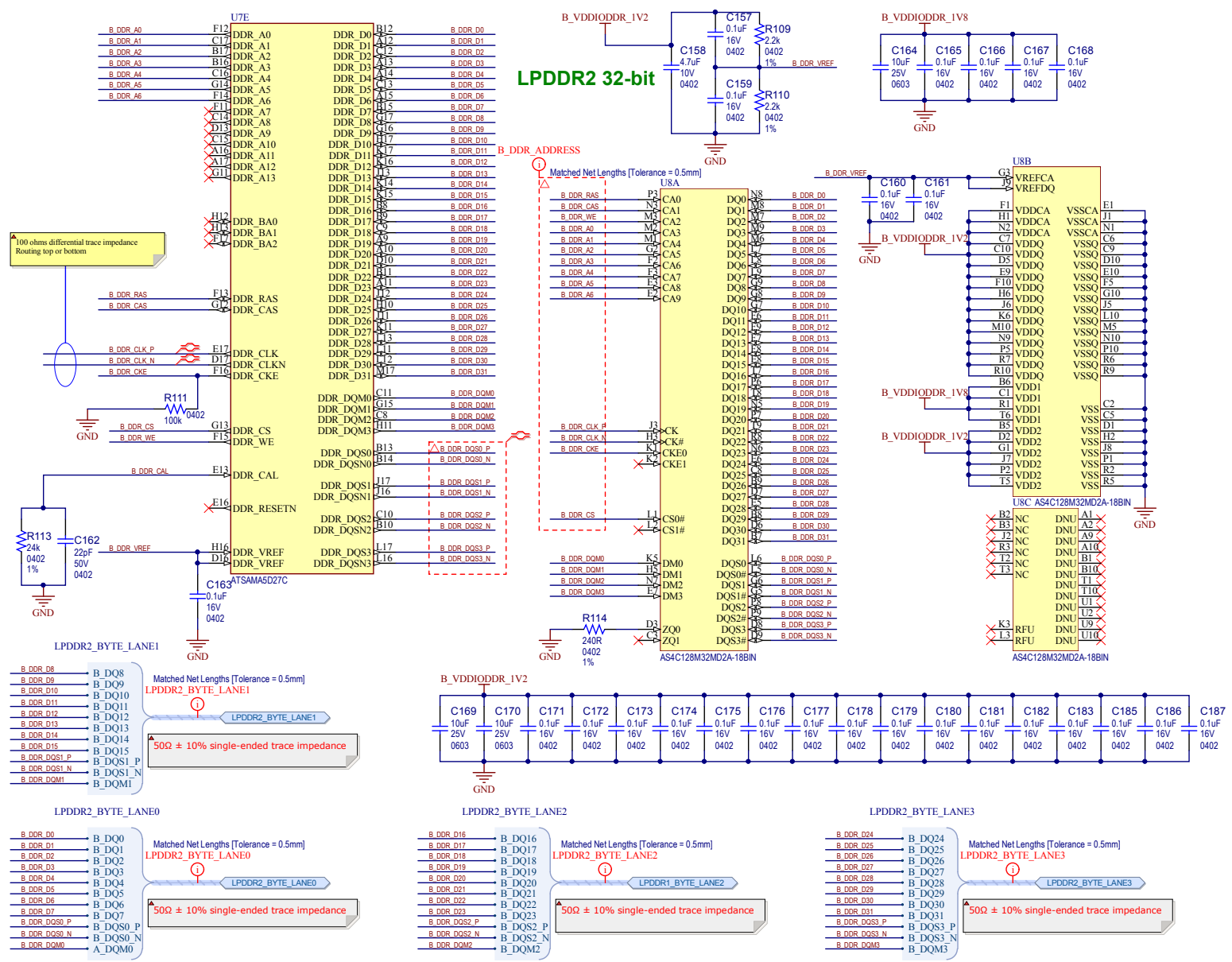
LPDDR2 32-bit
U7E
ATSAMA5D27C
U8A
AS4C128M32MD2A-18BIN
U8B
U8C AS4C128M32MD2A-18BIN
B_VDDIODDR_1V2
B_VDDIODDR_1V8
B_DDR_VREF
B_DDR_ADDRESS
Matched Net Lengths [Tolerance = 0.5mm]
100 ohms differential trace impedance
Routing top or bottom
R111 100k 0402
R113 24k 0402 1%
C162 22pF 50V 0402
C163 0.1uF 16V 0402
R114 240R 0402 1%
C157 0.1uF 16V 0402
R109 2.2k 0402 1%
C158 4.7uF 10V 0402
C159 0.1uF 16V 0402
R110 2.2k 0402 1%
C160 0.1uF 16V 0402
C161 0.1uF 16V 0402
C164 10uF 25V 0603
C165 0.1uF 16V 0402
C166 0.1uF 16V 0402
C167 0.1uF 16V 0402
C168 0.1uF 16V 0402
C169 10uF 25V 0603
C170 10uF 25V 0603
C171 0.1uF 16V 0402
C172 0.1uF 16V 0402
C173 0.1uF 16V 0402
C174 0.1uF 16V 0402
C175 0.1uF 16V 0402
C176 0.1uF 16V 0402
C177 0.1uF 16V 0402
C178 0.1uF 16V 0402
C179 0.1uF 16V 0402
C180 0.1uF 16V 0402
C181 0.1uF 16V 0402
C182 0.1uF 16V 0402
C183 0.1uF 16V 0402
C185 0.1uF 16V 0402
C186 0.1uF 16V 0402
C187 0.1uF 16V 0402
GND
LPDDR2_BYTE_LANE1
LPDDR2_BYTE_LANE0
LPDDR2_BYTE_LANE2
LPDDR2_BYTE_LANE3
50Ω ± 10% single-ended trace impedance