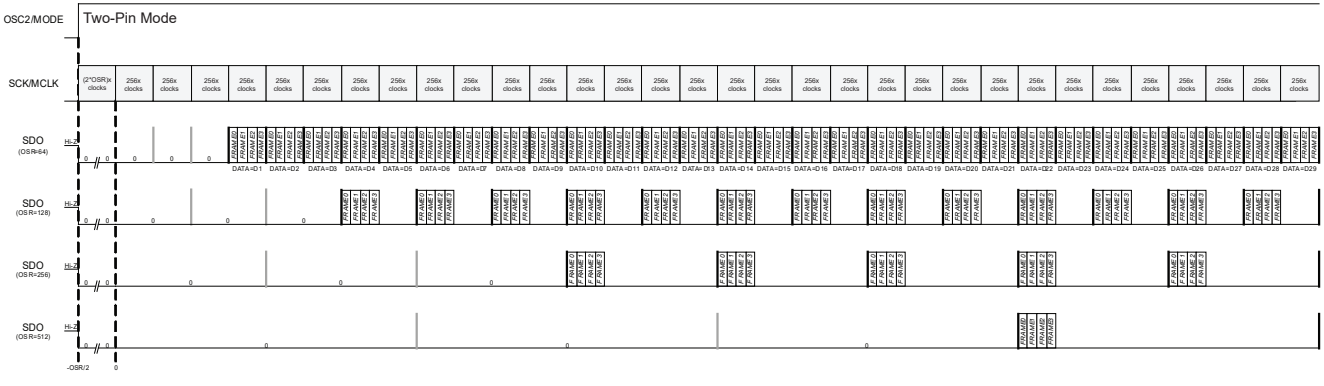
OSC2/MODE
Two-Pin Mode
SCK/MCLK
(2*OSR)x clocks
256x clocks
SDO (OSR=64)
SDO (OSR=128)
SDO (OSR=256)
SDO (OSR=512)
Hi-Z
0
-OSR/2
FRAME1
FRAME2
FRAME3
DATA=D1
DATA=D2
DATA=D3
DATA=D4
DATA=D5
DATA=D6
DATA=D7
DATA=D8
DATA=D9
DATA=D10
DATA=D11
DATA=D12
DATA=D13
DATA=D14
DATA=D15
DATA=D16
DATA=D17
DATA=D18
DATA=D19
DATA=D20
DATA=D21
DATA=D22
DATA=D23
DATA=D24
DATA=D25
DATA=D26
DATA=D27
DATA=D28
DATA=D29
Internal data ready (data is unsettled)
No frame is transmitted
Data ready
New data is available