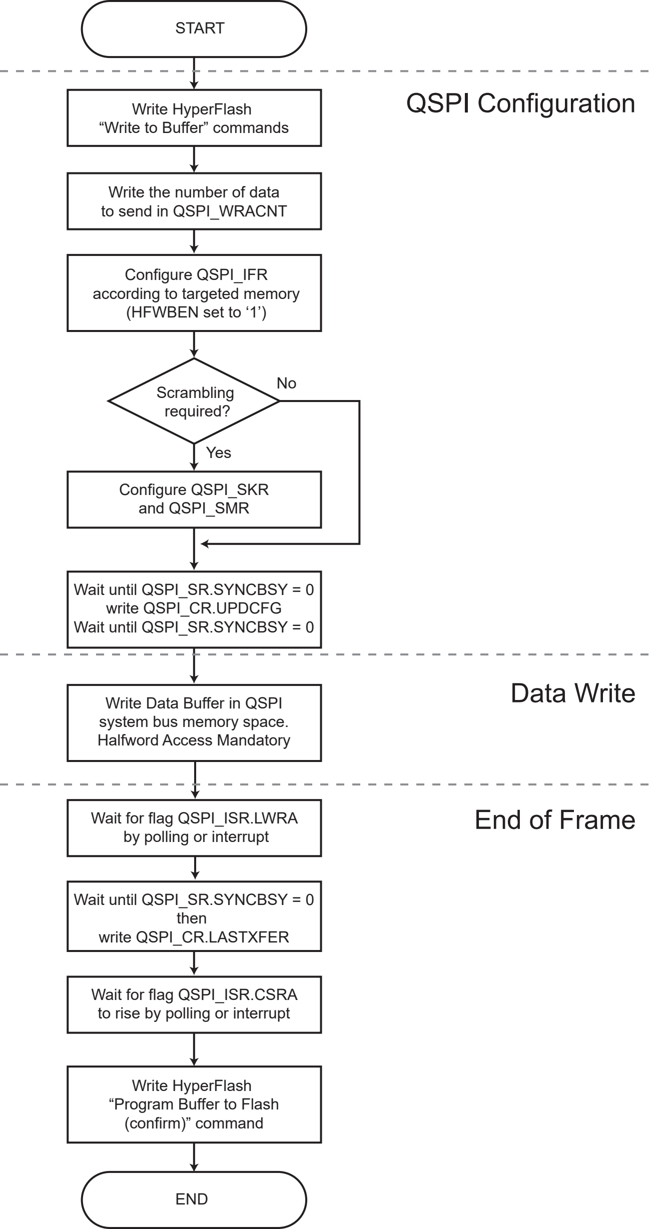
START
QSPI Configuration
Write HyperFlash “Write to Buffer” commands
Write the number of data to send in QSPI_WRACNT
Configure QSPI_IFR according to targeted memory (HFWBEN set to ‘1’)
Scrambling required?
No
Yes
Configure QSPI_SKR and QSPI_SMR
Wait until QSPI_SR.SYNCBSY = 0 write QSPI_CR.UPDCFG Wait until QSPI_SR.SYNCBSY = 0
Data Write
Write Data Buffer in QSPI system bus memory space. Halfword Access Mandatory
End of Frame
Wait for flag QSPI_ISR.LWRA by polling or interrupt
Wait until QSPI_SR.SYNCBSY = 0 then write QSPI_CR.LASTXFER
Wait for flag QSPI_ISR.CSRA to rise by polling or interrupt
Write HyperFlash “Program Buffer to Flash (confirm)” command
END