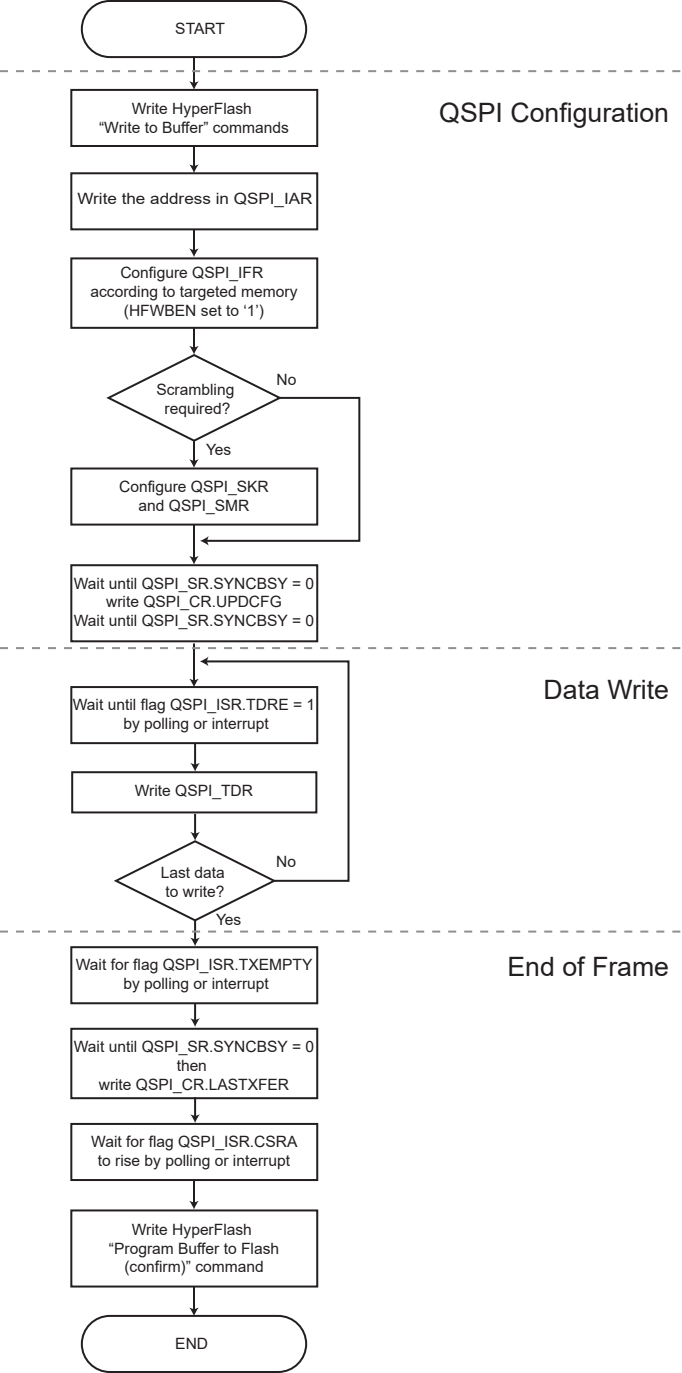
START
QSPI Configuration
Write HyperFlash
“Write to Buffer” commands
Write the address in QSPI_IAR
Configure QSPI_IFR
according to targeted memory
(HFWBEN set to ‘1’)
Scrambling
required?
No
Yes
Configure QSPI_SKR
and QSPI_SMR
Wait until QSPI_SR.SYNCBSY = 0
write QSPI_CR.UPDCFG
Wait until QSPI_SR.SYNCBSY = 0
Data Write
Wait until flag QSPI_ISR.TDRE = 1
by polling or interrupt
Write QSPI_TDR
Last data
to write?
No
Yes
End of Frame
Wait for flag QSPI_ISR.TXEMPTY
by polling or interrupt
Wait until QSPI_SR.SYNCBSY = 0
then
write QSPI_CR.LASTXFER
Wait for flag QSPI_ISR.CSRA
to rise by polling or interrupt
Write HyperFlash
“Program Buffer to Flash
(confirm)” command
END