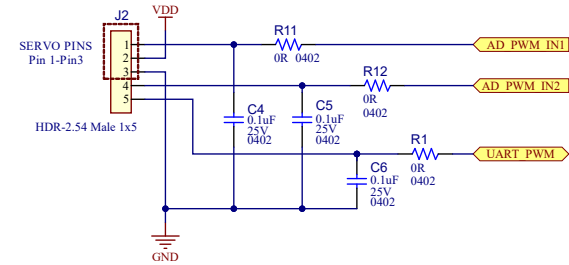
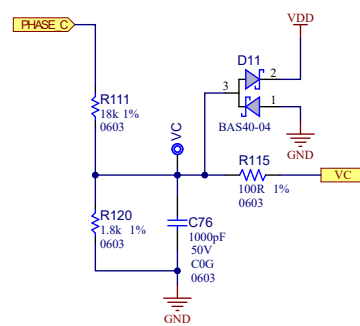
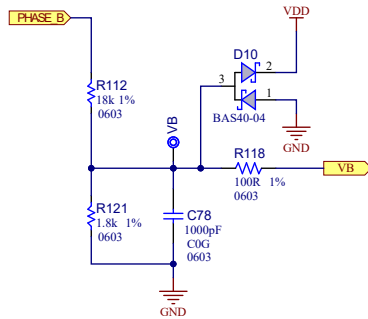
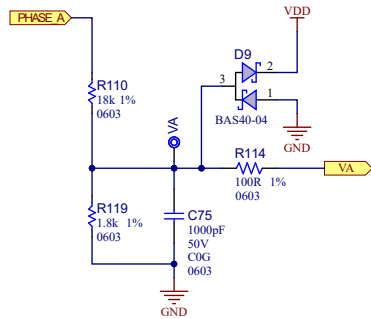


Ensure total power dissipation of the dsPIC33CDVC256MP506 (U9) does not exceed the specified value when internal 3.3V LDO (VREG- U9/U9B PIN#6) is used for powering dsPIC33CK256MP506 (U9A) by populating R54 instead of R19.

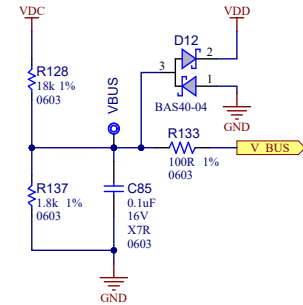
Resistor Configuration for the Supply Voltages:
 VREG : R19=DNP and R54=0R, +3.3V is derived from Internal VREG (U9/U9B Pin #6)
 : In this configuration, do not draw more than 20mA
 +3.3V_EXT : When R19=0R, R54=DNP, 3.3V is derived from External Source



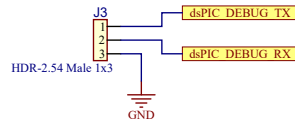
Phase Voltage Sensing



DC Bus Voltage Sensing



UART Communication Header



General Purpose LEDs

