

AT28HC64B: Package Marking Information

32-Lead PLCC	
Topside	Backside
● ATMEL AT28HC64B %%U-19803A YYWWNNN	

28-Lead SOIC	
Topside	Backside
ATMEL 19803A 28HC64B-%%U YYWWNNN ●	

Note: No backside markings

		%% = Access Time	
		70: 70 ns 90: 90 ns 12: 120 ns	
		YYWWNNN: Lot Trace Code YY: Year, WW: Work Week NNN: Assembly Trace Code	