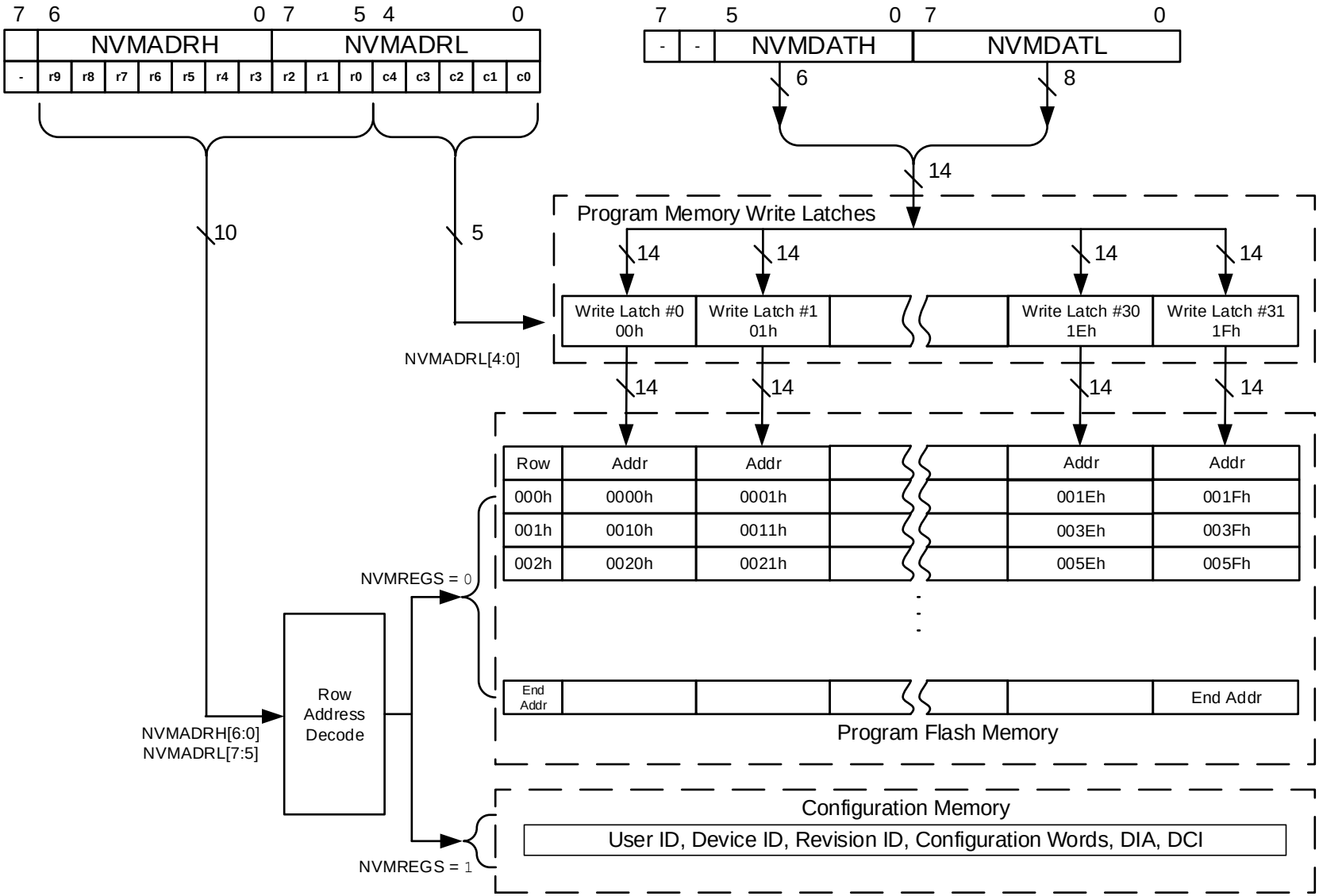
7 6 0 7 5 4 0
NVMADRH
NVMADRL
- r9 r8 r7 r6 r5 r4 r3 r2 r1 r0 c4 c3 c2 c1 c0
7 5 0 7 0
- - NVMDATH NVMDATL
6
8
14
Program Memory Write Latches
14 14 14 14
Write Latch #0 00h
Write Latch #1 01h
Write Latch #30 1Eh
Write Latch #31 1Fh
10
5
NVMADRL[4:0]
14 14 14 14
Row Addr Addr Addr Addr
000h 0000h 0001h 001Eh 001Fh
001h 0010h 0011h 003Eh 003Fh
002h 0020h 0021h 005Eh 005Fh
End Addr End Addr
Program Flash Memory
NVMREGS = 0
Row Address Decode
NVMADRH[6:0]
NVMADRL[7:5]
Configuration Memory
User ID, Device ID, Revision ID, Configuration Words, DIA, DCI
NVMREGS = 1