

INTERRUPT CONFIGURATION

Vector Number	Name	Use	Priority (1 = Lowest)	SubPriority	HandlerName
144	SPI2_TX (SPI2 Transmit Done)	<input type="checkbox"/>	1	0	SPI2_TX_Handler
145	UART2_FAULT (UART2 Fault)	<input type="checkbox"/>	1	0	UART2_FAULT_Handler
146	UART2_RX (UART2 Receive Done)	<input checked="" type="checkbox"/>	1	0	UART2_RX_Handler
147	UART2_TX (UART2 Transmit Done)	<input type="checkbox"/>	1	0	UART2_TX_Handler
148	I2C2_BUS (I2C2 Bus Collision Event)	<input type="checkbox"/>	1	0	I2C2_BUS_Handler
149	I2C2_SLAVE (I2C2 Slave Event)	<input type="checkbox"/>	1	0	I2C2_SLAVE_Handler
150	I2C2_MASTER (I2C2 Master Event)	<input type="checkbox"/>	1	0	I2C2_MASTER_Handler
151	CAN1 (Control Area Network 1)	<input type="checkbox"/>	1	0	CAN1_Handler
152	CAN2 (Control Area Network 2)	<input type="checkbox"/>	1	0	CAN2_Handler
153	ETHERNET (Ethernet)	<input type="checkbox"/>	1	0	ETHERNET_Handler
154	SPI3_FAULT (SPI3 Fault)	<input type="checkbox"/>	1	0	SPI3_FAULT_Handler
155	SPI3_RX (SPI3 Receive Done)	<input type="checkbox"/>	1	0	SPI3_RX_Handler
156	SPI3_TX (SPI3 Transmit Done)	<input type="checkbox"/>	1	0	SPI3_TX_Handler
157	UART3_FAULT (UART3 Fault)	<input type="checkbox"/>	1	0	UART3_FAULT_Handler
158	UART3_RX (UART3 Receive Done)	<input type="checkbox"/>	1	0	UART3_RX_Handler
159	UART3_TX (UART3 Transmit Done)	<input checked="" type="checkbox"/>	1	0	UART3_TX_Handler
160	I2C3_BUS (I2C3 Bus Collision Event)	<input type="checkbox"/>	1	0	I2C3_BUS_Handler
161	I2C3_SLAVE (I2C3 Slave Event)	<input type="checkbox"/>	1	0	I2C3_SLAVE_Handler