

in Table x Pin S... ◀ ▶ ▼ ✖

Configuration Options x

View: Root

UART6

- Operating Mode
- Stop Selection bit
- Parity and Data Selection bits
- High Baud Rate Enable bit
- UARTx Enable bits
- Clock Frequency
- Baud Rate

Non-blocking mode

1 Stop bit

8-bit data, no parity

High-Speed mode 4x baud clock enabled

UxTX and UxRX pins are enabled and used; UxCTS and UxRTS/UxBCLK pins are controlled by corresponding bits in the PORTx register

100,000,000

115,200

*** Baud Error = 0.0061 % ***

UART6 Peripheral Library **UART**