

Project Graph... Configuration Options

Plugin

UART6

Peripheral Library

UART

Operating Mode

Stop Selection bit

Parity and Data Selection bits

High Baud Rate Enable bit

UARTx Enable bits

Clock Frequency

Baud Rate

*** Baud Error = 0.0061 % ***

Blocking mode

1 Stop bit

8-bit data, no parity

High-Speed mode 4x baud clock enabled

UxTX and UxRX pins are enabled and used; UxCTS and UxRTS/UxBCLK pins are controlled by corresponding bits in the PORTx register

100,000,000

115,200

This screenshot shows the configuration options for the UART6 peripheral in a development environment. The 'Operating Mode' is set to 'Blocking mode'. The 'Baud Rate' is set to 115,200. A note at the bottom states a 'Baud Error = 0.0061 %'. The 'Clock Frequency' is listed as 100,000,000. The 'High Baud Rate Enable bit' is selected. The 'UART6' node in the tree view is highlighted with a green border.