

DMA Channels List

Channel Number	Enable	Trigger	Priority	Interrupt Enable
DMAC Channel 0	☒	UART4_RX	CHPRI0	☒
DMAC Channel 1	☒	Software Trigger	CHPRI1	☒
DMAC Channel 2	☐	Software Trigger	CHPRI0	☒
DMAC Channel 3	☐	Software Trigger	CHPRI0	☒
DMAC Channel 4	☐	Software Trigger	CHPRI0	☒
DMAC Channel 5	☐	Software Trigger	CHPRI0	☒
DMAC Channel 6	☐	Software Trigger	CHPRI0	☒
DMAC Channel 7	☐	Software Trigger	CHPRI0	☒