

Pin Table x Pin S... ◀ ▶ ▾ □

View: Root ▾

UART6
Peripheral Library
UART

Configuration Options x □

[-] [+]

[-] UART6

Operating Mode	Non-blocking mode ▾
Stop Selection bit	1 Stop bit ▾
Parity and Data Selection bits	8-bit data, no parity ▾
High Baud Rate Enable bit	High-Speed mode 4x baud clock enabled ▾
UARTx Enable bits	UxTX and UxRX pins are enabled and used; UxCTS and UxRTS/UxBCLK pins are controlled by corresponding bits in the PORTx register ▾
Clock Frequency	100,000,000 ▴ ▾
Baud Rate	115,200 ▴ ▾
*** Baud Error = 0.0061 % ***	