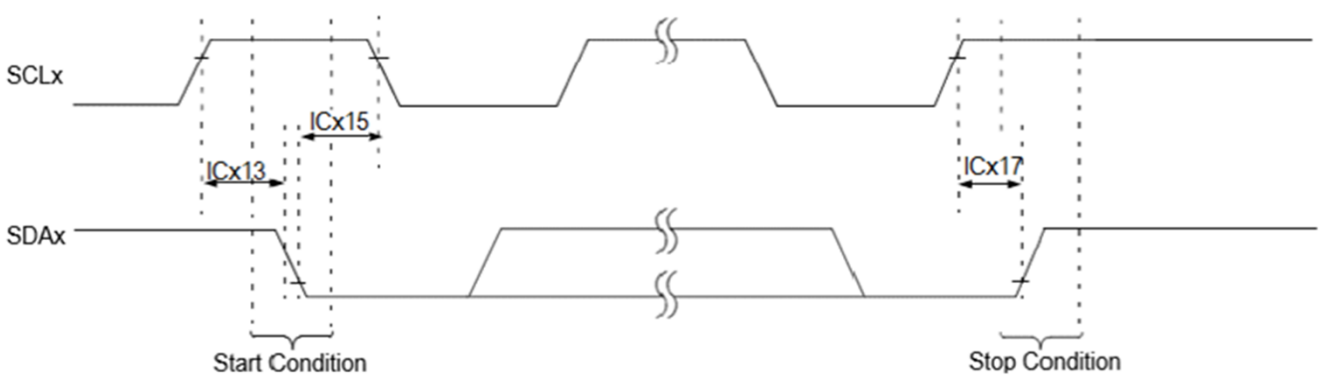




Slide 2

DDM0 `I2c_start_stop_bits_host_mode`
Dimitri Dambas - M50534, 2023-03-09T14:40:34.508