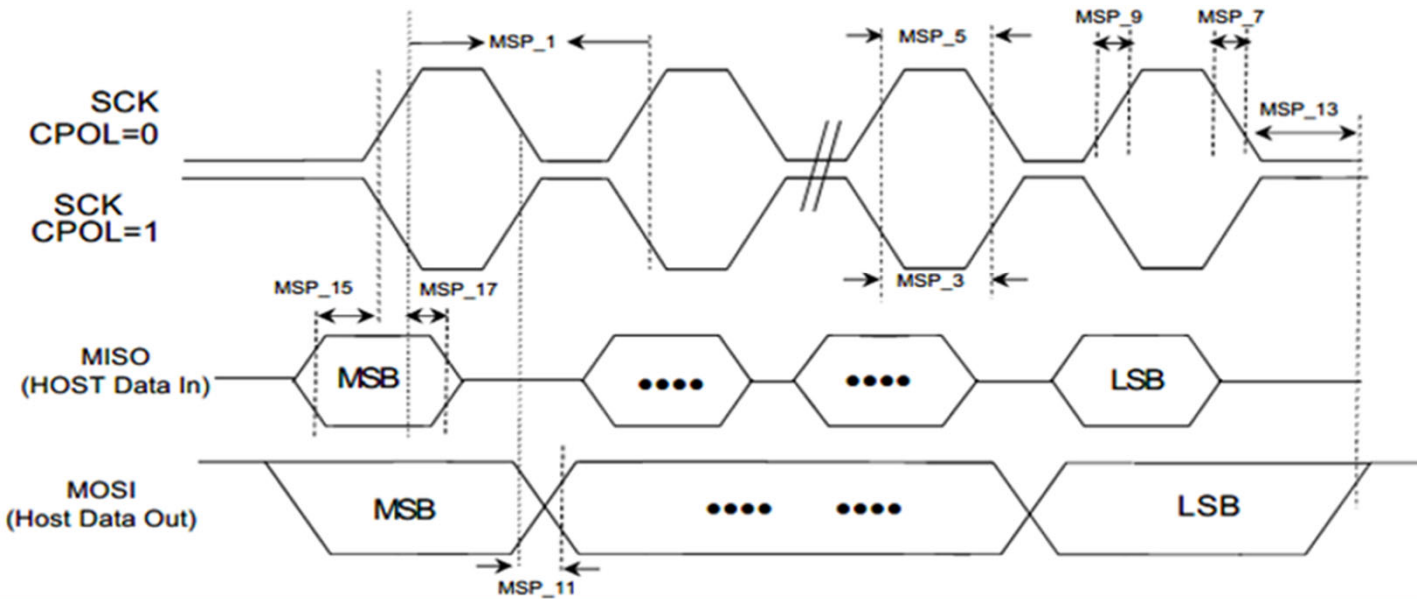


SPI HOST MODE (NCPHA = 0)

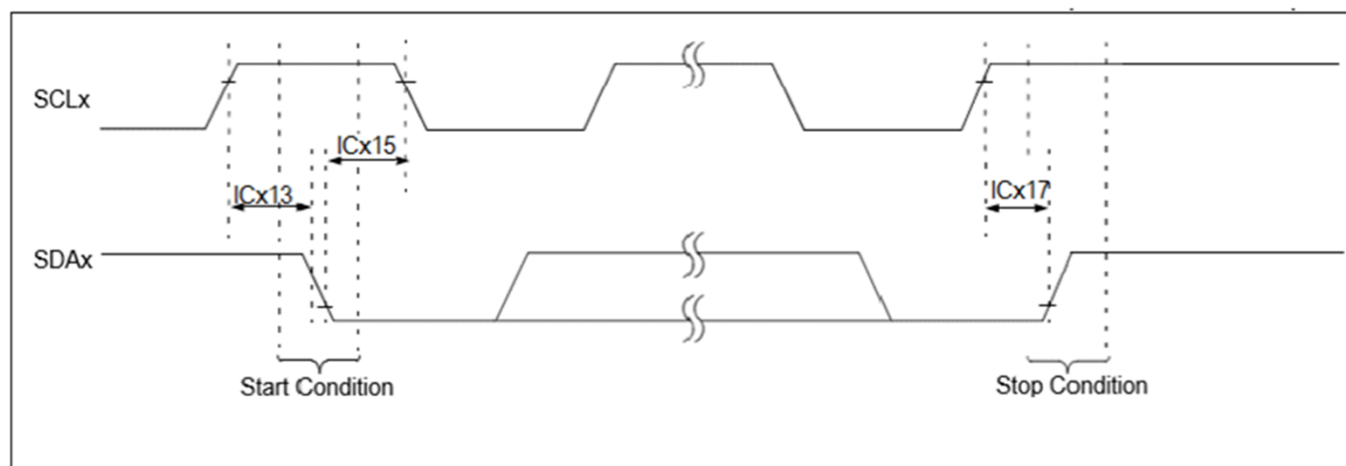


Slide 1

DDM0

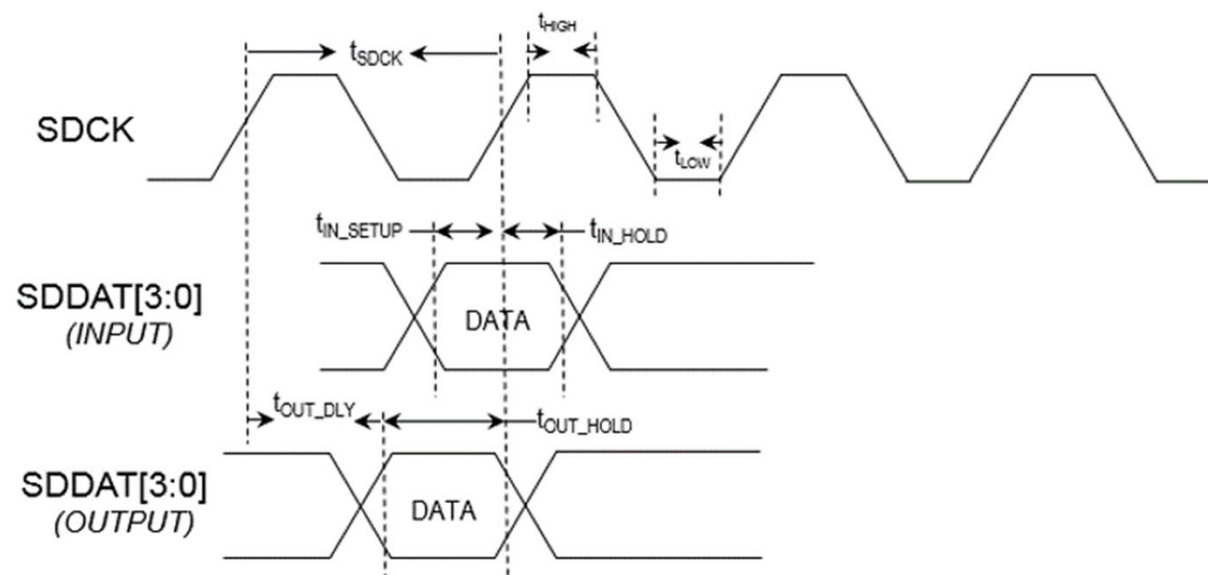
Spi_host_mode_ncpha_zero

Dimitri Dambas - M50534, 2023-03-09T14:40:19.388



Slide 2

DDM0 `I2c_start_stop_bits_host_mode`
Dimitri Dambas - M50534, 2023-03-09T14:40:34.508



Slide 3

DDM0

Hsmci_module

Dimitri Dambas - M50534, 2023-03-09T14:40:53.463