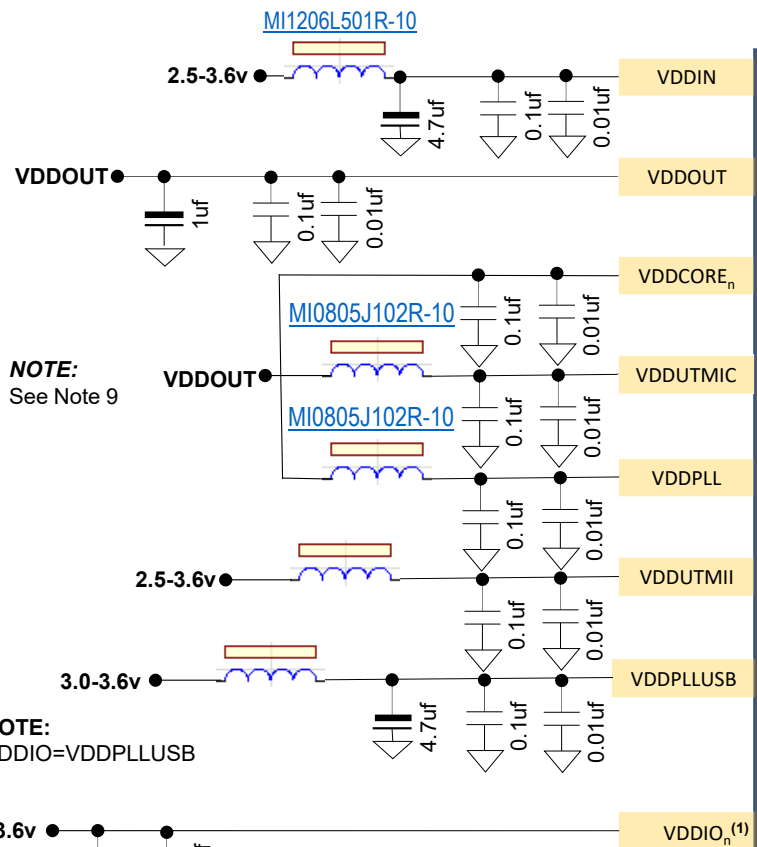
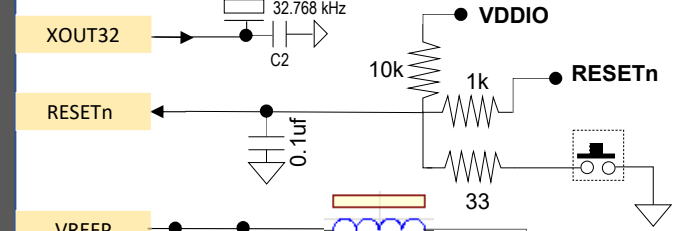
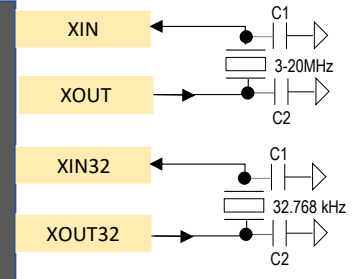


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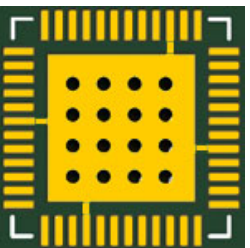
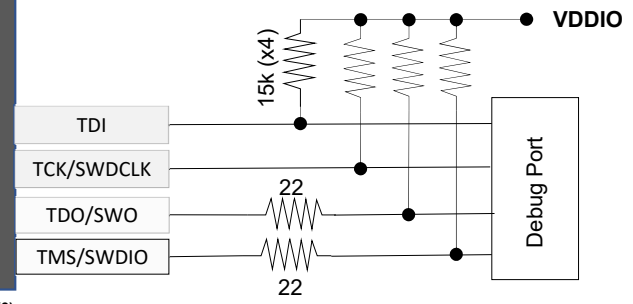
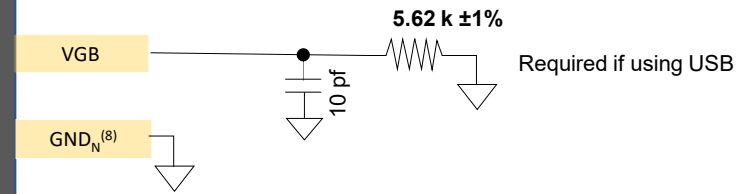
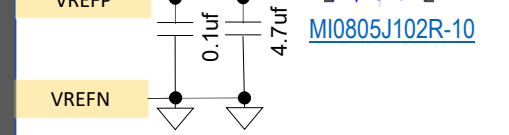


NOTE:
See Note 9

NOTE:
VDDIO=VDDPLLUSB



NOTE:
Optional Ext ADC VREF
(2.5v ≤ VREFP ≤ VDDIN)



PAD⁽³⁾

PAD⁽³⁾