AT27C256R: Package Marking Information

28-Lead PDIP	32-Lead PLCC

Topside	Backside	Topside	Backside
ATMEL AT27C256R %%U-34A03B YYWWNNN		ATMEL AT27C256R %%U-34A03B YYWWNNN	
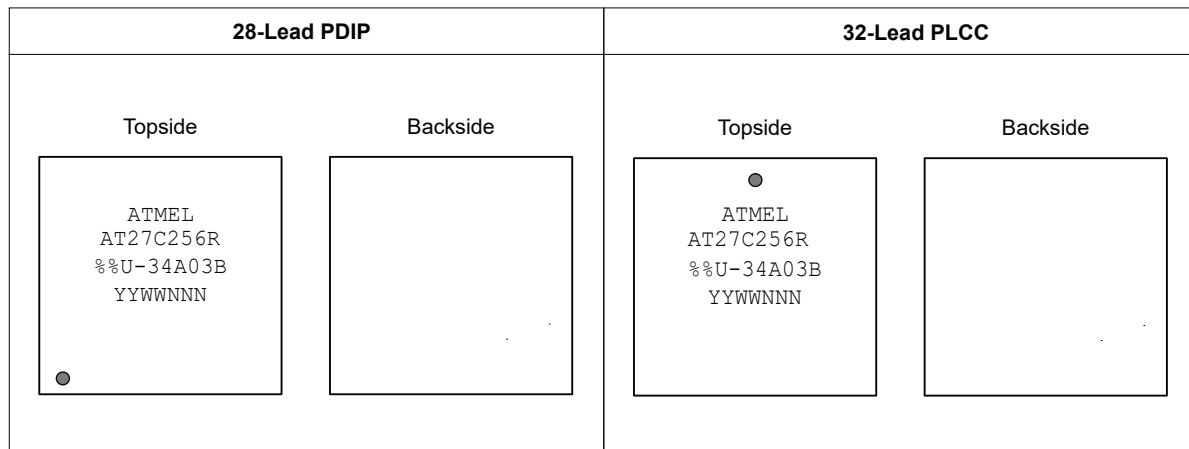
Note: no backside markings

		%% = Access Time	
		45: 45 ns 70: 70 ns	
		Lot Trace Code	
		YWWNNN: Lot Trace Code Y: Year, WW: Work Week NNN = Assembly Trace Code	