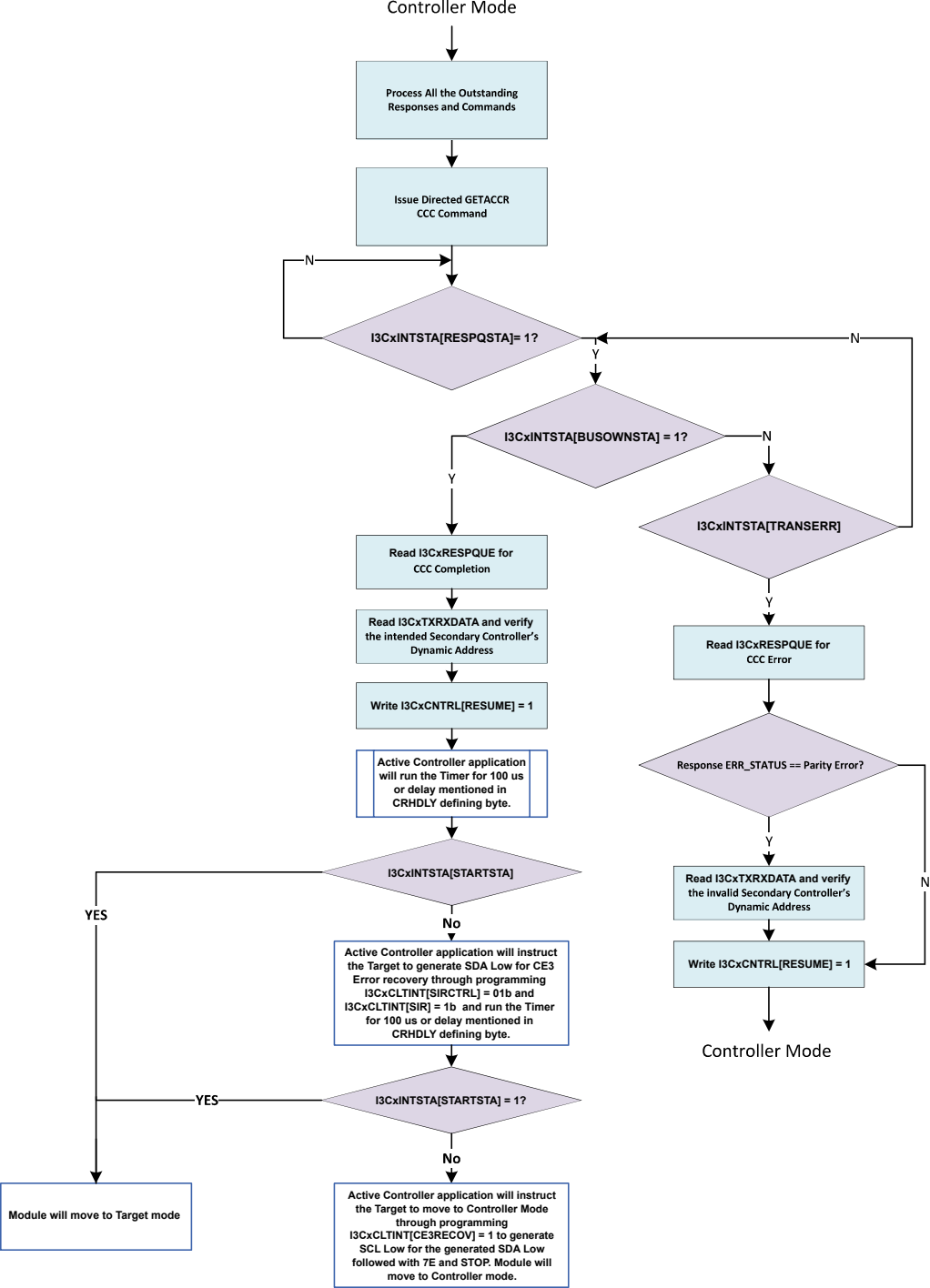
Controller Mode
Process All the Outstanding Responses and Commands
Issue Directed GETACCR CCC Command
N
I3CxINTSTA[RESPQSTA]= 1?
Y
N
I3CxINTSTA[BUSOWNSTA] = 1?
N
Y
I3CxINTSTA[TRANSERR]
Read I3CxRESPQUE for CCC Completion
Y
Read I3CxTXRXDATA and verify the intended Secondary Controller's Dynamic Address
Read I3CxRESPQUE for CCC Error
Write I3CxCNTRL[RESUME] = 1
Active Controller application will run the Timer for 100 us or delay mentioned in CRHDLY defining byte.
Response ERR_STATUS == Parity Error?
I3CxINTSTA[STARTSTA]
Y
N
YES
No
Read I3CxTXRXDATA and verify the invalid Secondary Controller's Dynamic Address
Active Controller application will instruct the Target to generate SDA Low for CE3 Error recovery through programming I3CxCLTINT[SIRCTRL] = 01b and I3CxCLTINT[SIR] = 1b and run the Timer for 100 us or delay mentioned in CRHDLY defining byte.
Write I3CxCNTRL[RESUME] = 1
Controller Mode
YES
I3CxINTSTA[STARTSTA] = 1?
No
Module will move to Target mode
Active Controller application will instruct the Target to move to Controller Mode through programming I3CxCLTINT[CE3RECOV] = 1 to generate SCL Low for the generated SDA Low followed with 7E and STOP. Module will move to Controller mode.