CACHE RAM

32 bit word 0	CACHEDATA0
32 bit word 1	CACHEDATA1
32 bit word 2	CACHEDATA2
32 bit word 3	CACHEDATA3
23 bit RAM address, RD, WR, Compare, Parity	CACHECTRL

TAG REGISTER

23 bit TAG word	TAGDATA
TAG address, RD bit, WR bit, Compare bit	TAGCTRL

ISB REGISTER

32 bit data 0 / ISB address element	ISBDATA0
ISB object address, RD bit, WR bit	ISBCTRL