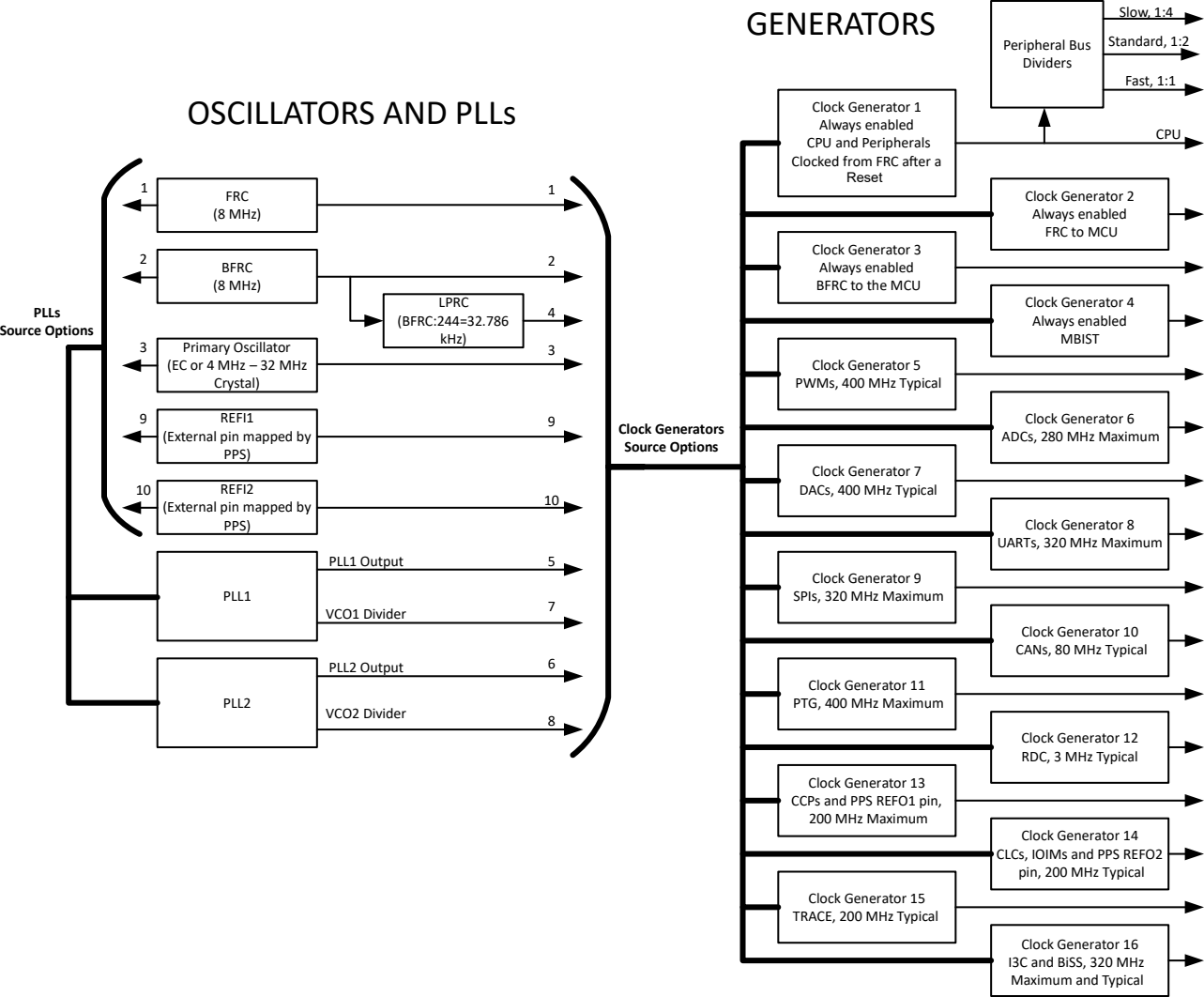
OSCILLATORS AND PLLs
GENERATORS
PLLs Source Options
FRC (8 MHz)
BFRC (8 MHz)
LPRC (BFRC:244=32.786 kHz)
Primary Oscillator (EC or 4 MHz – 32 MHz Crystal)
REFI1 (External pin mapped by PPS)
REFI2 (External pin mapped by PPS)
PLL1
PLL1 Output
VCO1 Divider
PLL2
PLL2 Output
VCO2 Divider
Clock Generators Source Options
Peripheral Bus Dividers
Slow, 1:4
Standard, 1:2
Fast, 1:1
CPU
Clock Generator 1 Always enabled CPU and Peripherals Clocked from FRC after a Reset
Clock Generator 2 Always enabled FRC to MCU
Clock Generator 3 Always enabled BFRC to the MCU
Clock Generator 4 Always enabled MBIST
Clock Generator 5 PWMs, 400 MHz Typical
Clock Generator 6 ADCs, 280 MHz Maximum
Clock Generator 7 DACs, 400 MHz Typical
Clock Generator 8 UARTs, 320 MHz Maximum
Clock Generator 9 SPIs, 320 MHz Maximum
Clock Generator 10 CANs, 80 MHz Typical
Clock Generator 11 PTG, 400 MHz Maximum
Clock Generator 12 RDC, 3 MHz Typical
Clock Generator 13 CCPs and PPS REFO1 pin, 200 MHz Maximum
Clock Generator 14 CLCs, IOIMs and PPS REFO2 pin, 200 MHz Typical
Clock Generator 15 TRACE, 200 MHz Typical
Clock Generator 16 I3C and BiSS, 320 MHz Maximum and Typical