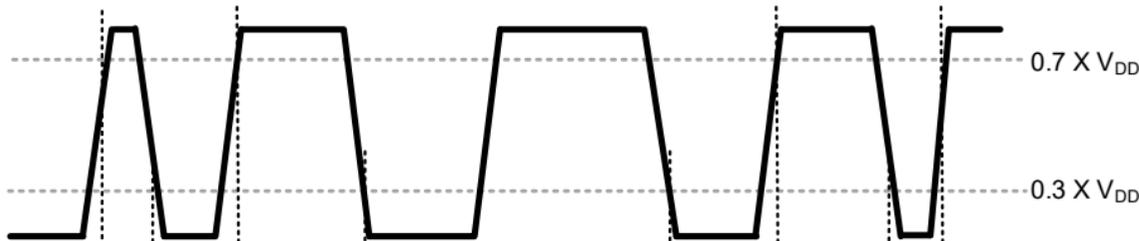


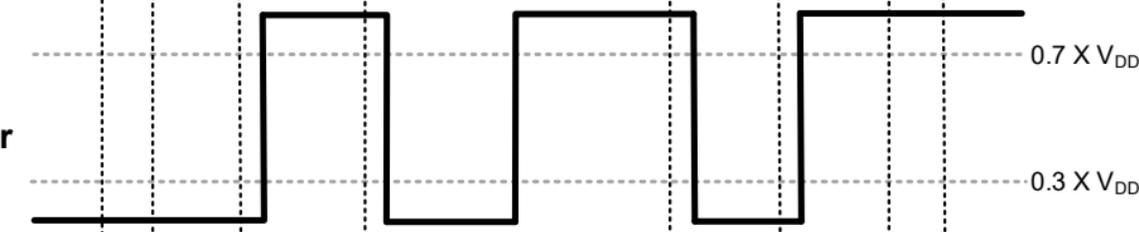
Note: Not to scale

Input to
SDA/SCL Pins

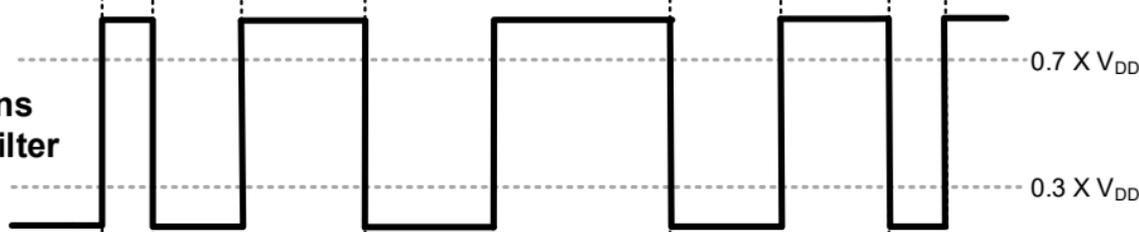


Legacy I²C
SDA/SCL Pins
After Spike Filter

Suppressed by
50 ns filter



I³C SDA/SCL Pins
Without Spike Filter



$t_{DIG_H_MIXED}$

t_{DIG_H}

t_{DIG_L}

$t_{DIG_L_MIXED}$