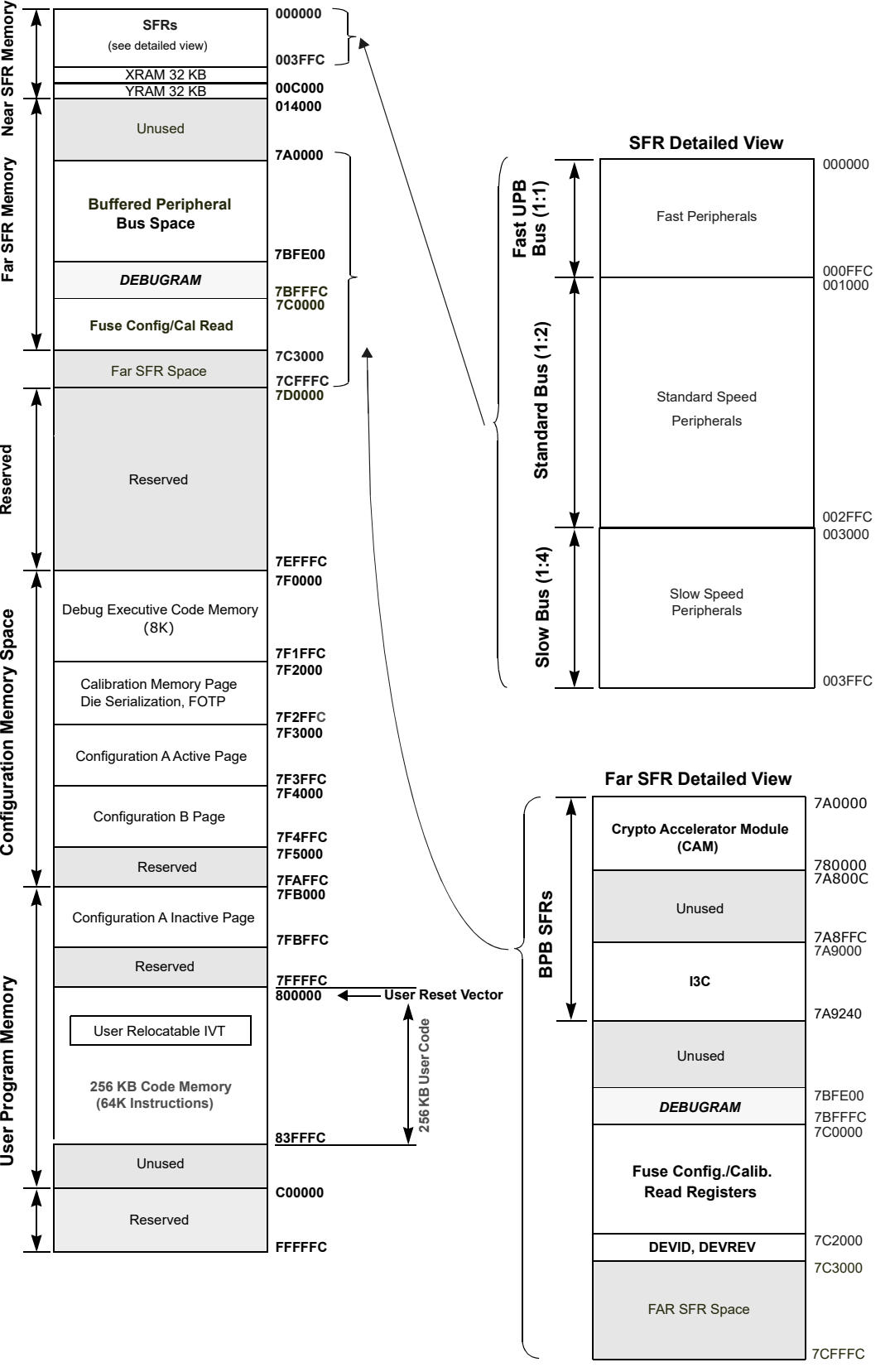
Near SFR Memory
Far SFR Memory
Reserved
Configuration Memory Space
User Program Memory
SFRs
(see detailed view)
XRAM 32 KB
YRAM 32 KB
Unused
Buffered Peripheral Bus Space
DEBUGRAM
Fuse Config/Cal Read
Far SFR Space
Reserved
Debug Executive Code Memory (8K)
Calibration Memory Page Die Serialization, FOTP
Configuration A Active Page
Configuration B Page
Reserved
Configuration A Inactive Page
Reserved
User Relocatable IVT
256 KB Code Memory (64K Instructions)
Unused
Reserved
000000
003FFC
00C000
014000
7A0000
7BFE00
7BFFFC
7C0000
7C3000
7CFFFC
7D0000
7EFFFC
7F0000
7F1FFC
7F2000
7F2FFC
7F3000
7F3FFC
7F4000
7F4FFC
7F5000
7FAFFC
7FB000
7FBFFC
7FFFFC
800000
83FFFC
C00000
FFFFFC
User Reset Vector
256 KB User Code
SFR Detailed View
Fast UPB Bus (1:1)
Standard Bus (1:2)
Slow Bus (1:4)
Fast Peripherals
Standard Speed Peripherals
Slow Speed Peripherals
000000
000FFC
001000
002FFC
003000
003FFC
Far SFR Detailed View
BPB SFRs
Crypto Accelerator Module (CAM)
Unused
I3C
Unused
DEBUGRAM
Fuse Config./Calib. Read Registers
DEVID, DEVREV
FAR SFR Space
7A0000
780000
7A800C
7A8FFC
7A9000
7A9240
7BFE00
7BFFFC
7C0000
7C2000
7C3000
7CFFFC