

A diagram of a microcontroller pinout. The chip is represented as a square with a central core and four quadrants. The central core is labeled 'Core and Memory Logic, Voltage Regulator, IO Buffer Power Rails'. The top-left and bottom-left quadrants are labeled 'Digital IOs'. The top-right and bottom-right quadrants are labeled 'Segment LCD IOs, Digital IOs and ADC/ACC Analog Inputs'. The bottom-right quadrant also includes 'VBAT Supply and IOs'. The chip is surrounded by various colored pads: dark blue on the top, bottom, and right sides; light grey on the left side; and a tan-colored pad on the right side that tapers towards the top and bottom.

Digital IOs

Segment LCD IOs,
Digital IOs
and ADC/ACC
Analog Inputs

Core and Memory Logic,
Voltage Regulator,
IO Buffer Power Rails

Digital IOs

Segment LCD IOs,
Digital IOs
and ADC/ACC
Analog Inputs

VBAT Supply and IOs