

BANK 60

1E00h	Core Registers	1E3Ah	Unimplemented Read as '0'
1E0Bh		1E3Ch	
1E0Ch	PPSLOCK	1E3Dh	CLCIN0PPS
1E0Dh	INTPPS	1E3Eh	CLCIN1PPS
1E0Eh	T0CKIPPS	1E3Fh	CLCIN2PPS
1E0Fh	T1CKIPPS	1E40h	CLCIN3PPS
1E10h	T1GPPS	1E41h	CK1PPS
1E11h	T3CKIPPS	1E42h	RX1PPS
1E12h	T3GPPS	1E43h	CK2PPS
1E13h	Unimplemented Read as '0'	1E44h	RX2PPS
1E18h		1E45h	—
1E19h		1E46h	—
1E1Ah	T2INPPS	1E47h	SSP1CLKPPS
1E1Bh	T4INPPS	1E48h	SSP1DATPPS
1E1Ch	—	1E49h	SSP1SSPPS
1E1Dh	—	1E4Ah	SSP2CLKPPS
1E1Eh	—	1E4Bh	SSP2DATPPS
1E1Fh	CCP1PPS	1E4Ch	SSP2SSPPS
1E20h	CCP2PPS	1E4Dh	Unimplemented Read as '0'
1E23h	Unimplemented Read as '0'	1E4Fh	
1E24h		1E50h	ADACTPPS
1E25h		1E51h	Unimplemented Read as '0'
1E26h	PWMIN0PPS	1E56h	
1E27h	PWMIN1PPS	1E57h	CLBCLKPPS
1E28h	PWM1ERSPPS	1E58h	CLBIN0PPS
1E29h	PWM2ERSPPS	1E59h	CLBIN1PPS
1E30h	Unimplemented Read as '0'	1E5Ah	CLBIN2PPS
1E31h		1E5Bh	CLBIN3PPS
1E32h		1E5Ch	Unimplemented Read as '0'
1E33h		1E6Fh	
1E34h		1E70h	Common RAM (Accesses 70h-7Fh)
1E35h		1E71h	
1E36h		1E72h	
1E37h		1E73h	
1E38h		1E74h	
1E39h		1E75h	
1E3Ah		1E76h	
1E3Bh		1E77h	

Legend:



Unimplemented data memory locations, read as '0'