

BANK 48		BANK 49		BANK 50		BANK 51		BANK 52		BANK 53		BANK 54		BANK 55	
1800h	Core Registers	1880h	Core Registers	1900h	Core Registers	1980h	Core Registers	1A00h	Core Registers	1A80h	Core Registers	1B00h	Core Registers	1B80h	Core Registers
180Bh															
180Ch		188Ch		190Ch		198Ch		1A0Ch		1A8Ch		1B0Ch		1B8Ch	
180Dh		188Dh		190Dh		198Dh		1A0Dh		1A8Dh		1B0Dh		1B8Dh	
180Eh		188Eh		190Eh		198Eh		1A0Eh		1A8Eh		1B0Eh		1B8Eh	
180Fh		188Fh		190Fh		198Fh		1A0Fh		1A8Fh		1B0Fh		1B8Fh	
1810h		1890h		1910h		1990h		1A10h		1A90h		1B10h		1B90h	
1811h		1891h		1911h		1991h		1A11h		1A91h		1B11h		1B91h	
1812h		1892h		1912h		1992h		1A12h		1A92h		1B12h		1B92h	
1813h		1893h		1913h		1993h		1A13h		1A93h		1B13h		1B93h	
1814h		1894h		1914h		1994h		1A14h		1A94h		1B14h		1B94h	
1815h		1895h		1915h		1995h		1A15h		1A95h		1B15h		1B95h	
1816h		1896h		1916h		1996h		1A16h		1A96h		1B16h		1B96h	
1817h		1897h		1917h		1997h		1A17h		1A97h		1B17h		1B97h	
1818h		1898h		1918h		1998h		1A18h		1A98h		1B18h		1B98h	
1819h		1899h		1919h		1999h		1A19h		1A99h		1B19h		1B99h	
181Ah		189Ah		191Ah		199Ah		1A1Ah		1A9Ah		1B1Ah		1B9Ah	
181Bh	189Bh	191Bh	199Bh	1A1Bh	1A9Bh	1B1Bh	1B9Bh								
181Ch	189Ch	191Ch	199Ch	1A1Ch	1A9Ch	1B1Ch	1B9Ch								
181Dh	189Dh	191Dh	199Dh	1A1Dh	1A9Dh	1B1Dh	1B9Dh								
181Eh	189Eh	191Eh	199Eh	1A1Eh	1A9Eh	1B1Eh	1B9Eh								
181Fh	189Fh	191Fh	199Fh	1A1Fh	1A9Fh	1B1Fh	1B9Fh								
1820h	Unimplemented Read as '0'	18A0h	Unimplemented Read as '0'	1920h	Unimplemented Read as '0'	19A0h	Unimplemented Read as '0'	1A20h	Unimplemented Read as '0'	1AA0h	Unimplemented Read as '0'	1B20h	Unimplemented Read as '0'	1BA0h	Unimplemented Read as '0'
186Fh															
1870h		18F0h		1970h		19F0h		1A70h		1AF0h		1B70h		1BF0h	
187Fh		18FFh		197Fh		19FFh		1A7Fh		1AFFh		1B7Fh		1BFFh	