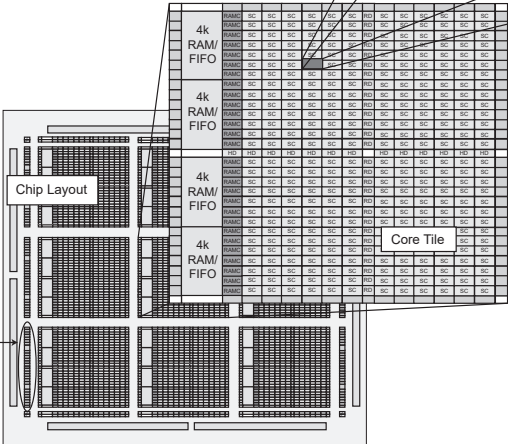


SuperCluster



Chip Layout

Core Tile

I/O Structure
See Figure 7