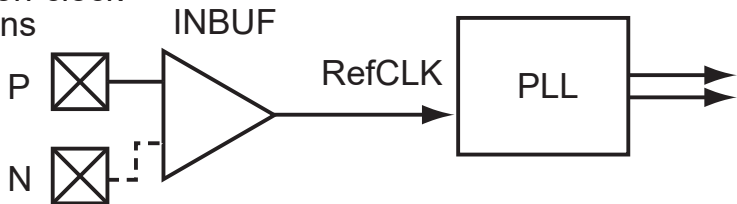
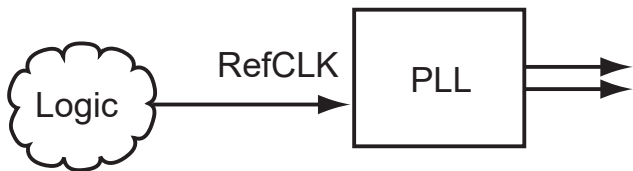


Regular, LVPECL, or LVDS IOPAD

Non-clock
Pins



Any macro from the core, except HCLK nets



For cascading

