

RomBOOT

-I- MDC clock is 3125000Hz

[INFO] Found PHY Micrel KSZ8081 at 0x00

[INFO] PHY Auto-Negotiation complete -> 100BaseT/Full

[TEST-START] DDR_TESTS

[TEST-INFO] Range -- from 0x20000000 to 0x28000000

[TEST-INFO] Total Size -- 128MB .. 1Gb

[TEST-INFO] DDR Range -- from 0x24000000 to 0x28000000

[TEST-INFO] Test1: -- Walking bit -- P

[TEST-INFO] Test2: -- Check for add bits stuck high -- P

[TEST-INFO] Test3: -- Check for add bits stuck low or shorted -- P

[TEST-INFO] Test4: -- Check pattern each location and invert it for the second pass -- P

[TEST-INFO] Test5: -- Check each location for the inverted pattern and zero it -- P

[TEST-END] DDR_TESTS -- -- -- P

[END-OF-TESTS]