

| Bit    | 7                           | 6                       | 5                           | 4                                              | 3                           | 2                           | 1                           | 0                            |
|--------|-----------------------------|-------------------------|-----------------------------|------------------------------------------------|-----------------------------|-----------------------------|-----------------------------|------------------------------|
|        | $\overline{\text{BOOTPOR}}$ | $\overline{\text{COE}}$ | $\overline{\text{CFGSCEN}}$ | <b><math>\overline{\text{DATASCEN}}</math></b> | $\overline{\text{SAFSCEN}}$ | $\overline{\text{APPSCEN}}$ | $\overline{\text{BOOTCOE}}$ | $\overline{\text{BOOTSCEN}}$ |
| Access | R/W                         | R/W                     | R/W                         | R/W                                            | R/W                         | R/W                         | R/W                         | R/W                          |
| Reset  | 1                           | 1                       | 1                           | 1                                              | 1                           | 1                           | 1                           | 1                            |