



COG3

 Easy Setup

 Registers

Hardware Settings

Enable COG


Mode Half-Bridge mode


Clock Source FOSC

Max Cumulative Uncertainty 3

Enable COG Interrupt

PWM12

 Easy Setup

 Registers

Hardware Settings

Enable PWM

PWM

Output Polarity active_hi


Mode standard_PWM


Clock

Clock Source FOSC

Prescaler No_Prescaler

OPA3

 Easy Setup

 Registers

Hardware Settings

Enable OPAMP

Channel Select

Positive Channel DAC5_out

Negative Channel OPA2IN0