

Tool	Device	Interface	Device signature	Target Voltage
nEDBG	AVR128DB48	UPDI	0x1E970C	3.3 V
		Apply	Read	Read

Interface settings

Tool information

Tool settings

Device information

Memories

Fuses

Lock bits

Production file

Fuse Name	Value
<input checked="" type="checkbox"/> BODCFG.ACTIVE	BOD disabled
<input checked="" type="checkbox"/> BODCFG.LVL	1.9V
<input checked="" type="checkbox"/> BODCFG.SAMPFREQ	Sample frequency is 128 Hz
<input checked="" type="checkbox"/> BODCFG.SLEEP	BOD disabled
<input checked="" type="checkbox"/> OSCCFG.CLKSEL	1-32MHz internal oscillator
<input checked="" type="checkbox"/> SYSCFG0.CRCSEL	Enable CRC32
<input checked="" type="checkbox"/> SYSCFG0.CRCSRC	No CRC
<input checked="" type="checkbox"/> SYSCFG0.EESAVE	<input checked="" type="checkbox"/>
<input checked="" type="checkbox"/> SYSCFG0.RSTPINCFG	GPIO mode
<input checked="" type="checkbox"/> SYSCFG1.MVSYSCFG	Device used in a dual supply configuration
<input checked="" type="checkbox"/> SYSCFG1.SUT	0 ms
<input checked="" type="checkbox"/> WDTCFG.PERIOD	Watch-Dog timer Off
<input checked="" type="checkbox"/> WDTCFG.WINDOW	Window mode off

Fuse Register Value

 Auto read Verify after programming

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Program

Verify

Read