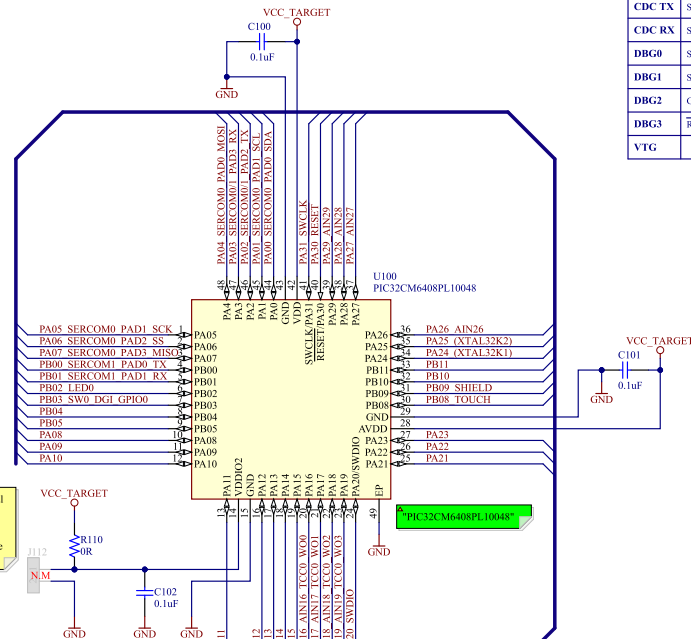
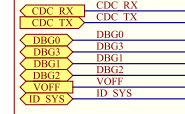


PIC32CM6408PL10048

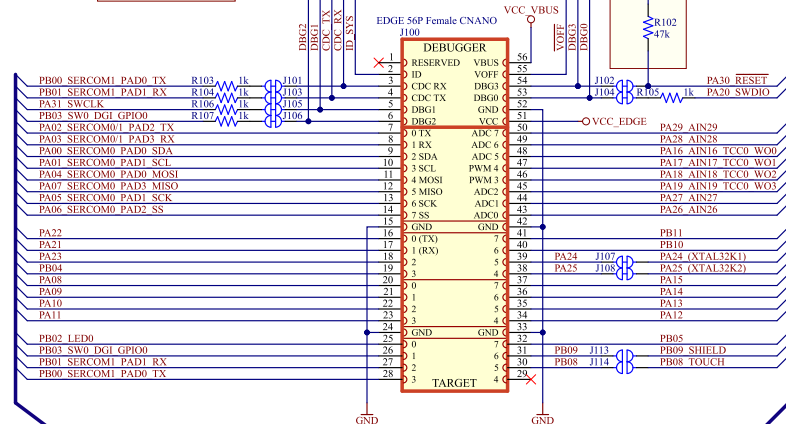
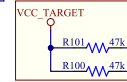


PIC32CM6408PL10048		
Debugger	Name	Pin
CDC TX	SERCOM1 PAD1	PB01
CDC RX	SERCOM1 PAD0	PB00
DBG0	SWDIO	PA20
DBG1	SWCLK	PA31
DBG2	GPIO0	PB03
DBG3	RESET	PA30
VTG	1.8V - 5.5V	

DEBUGGER CONNECTIONS



PROG/DEBUG Pull



Remember to remove R110 to disconnect on-board power before applying power to J112.

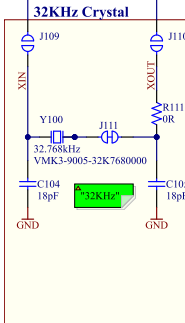
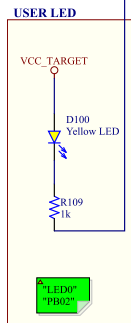
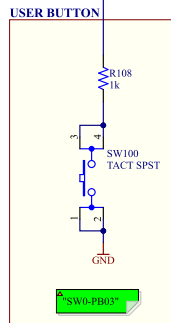
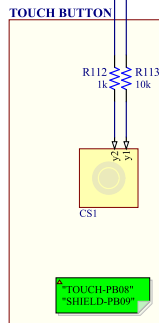
NOTE on UART/CDC:

RX/TX on the header denotes the input/output direction of the signal respective to it's source.

CDC TX is output from the DEBUGGER.
CDC RX is input to the DEBUGGER.
TX is output from the TARGET device.
RX is input to the TARGET device.

NOTE on 12C:

No pull-ups on board. Pull-ups must be mounted close to client device(s).



CIRCUIT DESIGNED FOR XOSC32K
HP MODE

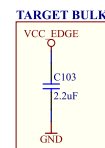
Crystal datasheet:
Ccrystal = 12.5pF
max ESR = 70kOhm
Accuracy +20ppm

TEMPLATE datasheet:
 Cxin = 5.5pF
 Cxout = 5.5pF
 $Cl \approx 1 / ((1/5.5pF) + (1/5.5pF)) \approx 2.75pF$
 Maximum Load = 18pF
 Maximum ESR = 100kOhm

Estimated $C_{pcb} = 0.5\text{pF}$

Estimated load
 $C = 2 (C_{\text{crystal}} - C_{\text{para}} - C_{\text{peb}})$
 $C = 2 (12.5\text{pF} - 2.75\text{pF} - 0.5\text{pF})$
 $C = 18.5\text{pF}$

Selected in design after verification
C= TBD



Project Owner:

SLT

PCB Layout Contact:
CLT

SLT

	Project Title
--	---------------

PIC32CM PL10 Curiosity Nano

Sheet Title

Target MCU

J

Size

Id #: 02-01489


MICROCHIP

NANO Template Revision 2.10

Designed with

Altium

File: PIC32CM_PL10_Curiosity_Nano_Target_MCU_SchDoc