

SPI Host:
Microcontroller

Data Clock (SCK)

Data Output (SO)

Data Input (SI)

SI

SO

SCK

Client 0
AT25XXX

$\overline{\text{CS}}$

SI

SO

SCK

Client 1
AT25XXX

$\overline{\text{CS}}$

SI

SO

SCK

Client 2
AT25XXX

$\overline{\text{CS}}$

SI

SO

SCK

Client 3
AT25XXX

$\overline{\text{CS}}$

$\overline{\text{CS3}}$

$\overline{\text{CS2}}$

$\overline{\text{CS1}}$

$\overline{\text{CS0}}$

