

SDADC

Select Prescaler

Peripheral clock divided by 2

Over Sampling Ratio

Over Sampling Ratio is 64

**** Conversion Time is 10.0 μ S ****

Select Reference

Internal Bandgap Reference

Select Conversion Trigger

Free Run

MUX Selection

SDADC AIN0 Pin

Enable Result Ready Interrupt

☐

Enable Result Ready Event Out

☐

Window Monitor Configurations

Sleep Configurations