	DAINK 02		
1F00h		1F3Bh	SLRCONA
	Core registers	1F3Ch	INLVLA
1F0Bh		1F3Dh	IOCAP
1F0Ch	Unimplemented	1F3Eh	IOCAN
	Read as '0'	1F3Fh	IOCAF
1F0Fh	Neau as U	1F40h	Unimplemented
1F10h	RA0PPS		Read as '0'
1F11h	RA1PPS	1F42h	
1F12h	RA2PPS	1F43h	ANSELB ⁽¹⁾
1F13h	RA3PPS	1F44h	WPUB ⁽¹⁾
1F14h	RA4PPS	1F45h	ODCONB ⁽¹⁾
1F15h	RA5PPS	1F46h	SLRCONB ⁽¹⁾
1F16h	Haiman la manada al	1F47h	INLVLB ⁽¹⁾
	Unimplemented Read as '0'	1F48h	IOCBP ⁽¹⁾
1F1Bh	Neau as 0	1F49h	IOCBN ⁽¹⁾
1F1Ch	RB4PPS ⁽¹⁾	1F4Ah	IOCBF ⁽¹⁾
1F1Dh	RB5PPS ⁽¹⁾	1F4Bh	Unimplemented
1F1Eh	RB6PPS ⁽¹⁾		Read as '0'
1F1Fh	RB7PPS ⁽¹⁾	1F4Dh	
1F1Fh 1F20h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾	1F4Dh 1F4Eh	ANSELC ⁽²⁾
	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾		ANSELC ⁽²⁾ WPUC ⁽²⁾
1F20h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾	1F4Eh	ANSELC ⁽²⁾ WPUC ⁽²⁾
1F20h 1F21h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾	1F4Eh 1F4Fh	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾
1F20h 1F21h 1F22h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾ RC4PPS ⁽²⁾	1F4Eh 1F4Fh 1F50h	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾ SLRCONC ⁽²⁾ INLVLC ⁽²⁾
1F20h 1F21h 1F22h 1F23h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾ RC4PPS ⁽²⁾	1F4Eh 1F4Fh 1F50h 1F51h	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾ SLRCONC ⁽²⁾ INLVLC ⁽²⁾
1F20h 1F21h 1F22h 1F23h 1F24h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾ RC4PPS ⁽²⁾ RC5PPS ⁽²⁾	1F4Eh 1F4Fh 1F50h 1F51h 1F52h	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾ SLRCONC ⁽²⁾ INLVLC ⁽²⁾ IOCCP ⁽²⁾
1F20h 1F21h 1F22h 1F23h 1F24h 1F25h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾ RC4PPS ⁽²⁾	1F4Eh 1F4Fh 1F50h 1F51h 1F52h 1F53h	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾ SLRCONC ⁽²⁾ INLVLC ⁽²⁾
1F20h 1F21h 1F22h 1F23h 1F24h 1F25h 1F26h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾ RC4PPS ⁽²⁾ RC5PPS ⁽²⁾ RC6PPS ⁽¹⁾ RC7PPS ⁽¹⁾	1F4Eh 1F4Fh 1F50h 1F51h 1F52h 1F53h 1F54h	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾ SLRCONC ⁽²⁾ INLVLC ⁽²⁾ IOCCP ⁽²⁾ IOCCN ⁽²⁾
1F20h 1F21h 1F22h 1F23h 1F24h 1F25h 1F26h 1F27h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾ RC4PPS ⁽²⁾ RC5PPS ⁽²⁾ RC6PPS ⁽¹⁾	1F4Eh 1F4Fh 1F50h 1F51h 1F52h 1F53h 1F54h 1F55h	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾ SLRCONC ⁽²⁾ INLVLC ⁽²⁾ IOCCP ⁽²⁾
1F20h 1F21h 1F22h 1F23h 1F24h 1F25h 1F26h 1F27h 1F28h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾ RC4PPS ⁽²⁾ RC5PPS ⁽²⁾ RC6PPS ⁽¹⁾ RC7PPS ⁽¹⁾	1F4Eh 1F4Fh 1F50h 1F51h 1F52h 1F53h 1F54h 1F55h 1F56h	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾ SLRCONC ⁽²⁾ INLVLC ⁽²⁾ IOCCP ⁽²⁾ IOCCN ⁽²⁾ IOCCF ⁽²⁾ Unimplemented
1F20h 1F21h 1F22h 1F23h 1F24h 1F25h 1F26h 1F27h 1F28h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾ RC4PPS ⁽²⁾ RC5PPS ⁽²⁾ RC6PPS ⁽¹⁾ RC7PPS ⁽¹⁾ Unimplemented Read as '0'	1F4Eh 1F4Fh 1F50h 1F51h 1F52h 1F53h 1F54h 1F55h 1F56h	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾ SLRCONC ⁽²⁾ INLVLC ⁽²⁾ IOCCP ⁽²⁾ IOCCN ⁽²⁾ IOCCF ⁽²⁾ Unimplemented Read as '0'
1F20h 1F21h 1F22h 1F23h 1F24h 1F25h 1F26h 1F27h 1F28h 1F37h 1F37h 1F37h	RB7PPS ⁽¹⁾ RC0PPS ⁽²⁾ RC1PPS ⁽²⁾ RC2PPS ⁽²⁾ RC3PPS ⁽²⁾ RC4PPS ⁽²⁾ RC5PPS ⁽²⁾ RC6PPS ⁽¹⁾ RC7PPS ⁽¹⁾ Unimplemented Read as '0' ANSELA	1F4Eh 1F4Fh 1F50h 1F51h 1F52h 1F53h 1F54h 1F55h 1F56h	ANSELC ⁽²⁾ WPUC ⁽²⁾ ODCONC ⁽²⁾ SLRCONC ⁽²⁾ INLVLC ⁽²⁾ IOCCP ⁽²⁾ IOCCN ⁽²⁾ IOCCF ⁽²⁾ Unimplemented Read as '0' Common RAM

Note: 1. 20-pin devices only 2. 14/20-pin devices only

Legend:

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