

BANK 40		BANK 41		BANK 42		BANK 43		BANK 44		BANK 45		BANK 46		BANK 47	
1400h	Core Registers	1480h	Core Registers	1500h	Core Registers	1580h	Core Registers	1600h	Core Registers	1680h	Core Registers	1700h	Core Registers	1780h	Core Registers
140Bh		148Bh		150Bh		158Bh		160Bh		168Bh		170Bh		178Bh	
140Ch	—	148Ch	—	150Ch	—	158Ch	—	160Ch	—	168Ch	—	170Ch	—	178Ch	—
140Dh	—	148Dh	—	150Dh	—	158Dh	—	160Dh	—	168Dh	—	170Dh	—	178Dh	—
140Eh	—	148Eh	—	150Eh	—	158Eh	—	160Eh	—	168Eh	—	170Eh	—	178Eh	—
140Fh	—	148Fh	—	150Fh	—	158Fh	—	160Fh	—	168Fh	—	170Fh	—	178Fh	—
1410h	—	1490h	—	1510h	—	1590h	—	1610h	—	1690h	—	1710h	—	1790h	—
1411h	—	1491h	—	1511h	—	1591h	—	1611h	—	1691h	—	1711h	—	1791h	—
1412h	—	1492h	—	1512h	—	1592h	—	1612h	—	1692h	—	1712h	—	1792h	—
1413h	—	1493h	—	1513h	—	1593h	—	1613h	—	1693h	—	1713h	—	1793h	—
1414h	—	1494h	—	1514h	—	1594h	—	1614h	—	1694h	—	1714h	—	1794h	—
1415h	—	1495h	—	1515h	—	1595h	—	1615h	—	1695h	—	1715h	—	1795h	—
1416h	—	1496h	—	1516h	—	1596h	—	1616h	—	1696h	—	1716h	—	1796h	—
1417h	—	1497h	—	1517h	—	1597h	—	1617h	—	1697h	—	1717h	—	1797h	—
1418h	—	1498h	—	1518h	—	1598h	—	1618h	—	1698h	—	1718h	—	1798h	—
1419h	—	1499h	—	1519h	—	1599h	—	1619h	—	1699h	—	1719h	—	1799h	—
141Ah	—	149Ah	—	151Ah	—	159Ah	—	161Ah	—	169Ah	—	171Ah	—	179Ah	—
141Bh	—	149Bh	—	151Bh	—	159Bh	—	161Bh	—	169Bh	—	171Bh	—	179Bh	—
141Ch	—	149Ch	—	151Ch	—	159Ch	—	161Ch	—	169Ch	—	171Ch	—	179Ch	—
141Dh	—	149Dh	—	151Dh	—	159Dh	—	161Dh	—	169Dh	—	171Dh	—	179Dh	—
141Eh	—	149Eh	—	151Eh	—	159Eh	—	161Eh	—	169Eh	—	171Eh	—	179Eh	—
141Fh	—	149Fh	—	151Fh	—	159Fh	—	161Fh	—	169Fh	—	171Fh	—	179Fh	—
1420h	Unimplemented Read as '0'	14A0h	Unimplemented Read as '0'	1520h	Unimplemented Read as '0'	15A0h	Unimplemented Read as '0'	1620h	Unimplemented Read as '0'	16A0h	Unimplemented Read as '0'	1720h	Unimplemented Read as '0'	17A0h	Unimplemented Read as '0'
146Fh		14EFh		156Fh		15EFh		166Fh		16EFh		176Fh		17EFh	
1470h		14F0h		1570h		15F0h		1670h		16F0h		1770h		17F0h	
147Fh		14FFh		157Fh		15FFh		167Fh		16FFh		177Fh		17FFh	
	Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)		Common RAM (Accesses 70h-7Fh)

Legend:

Unimplemented data memory locations, read as '0'