

Kit Window x Start Page x Project Graph x initialization.c x

Source History

```
112 #pragma config NVMCTRL_BCREN = SET
113 #pragma config BOOTROM_BOCORCRC = 0xdde78140U
114 #pragma config BOOTROM_CEKEY0_0 = 0xffffffffU
115 #pragma config BOOTROM_CEKEY0_1 = 0xffffffffU
116 #pragma config BOOTROM_CEKEY0_2 = 0xffffffffU
117 #pragma config BOOTROM_CEKEY0_3 = 0xffffffffU
118 #pragma config BOOTROM_CEKEY1_0 = 0x22222222U
119 #pragma config BOOTROM_CEKEY1_1 = 0x22222222U
120 #pragma config BOOTROM_CEKEY1_2 = 0x22222222U
121 #pragma config BOOTROM_CEKEY1_3 = 0x22222222U
122 #pragma config BOOTROM_CEKEY2_0 = 0x22222222U
123 #pragma config BOOTROM_CEKEY2_1 = 0x22222222U
124 #pragma config BOOTROM_CEKEY2_2 = 0x22222222U
125 #pragma config BOOTROM_CEKEY2_3 = 0x22222222U
126 #pragma config BOOTROM_CRCKEY_0 = 0xffffffffU
127 #pragma config BOOTROM_CRCKEY_1 = 0xffffffffU
128 #pragma config BOOTROM_CRCKEY_2 = 0xffffffffU
129 #pragma config BOOTROM_CRCKEY_3 = 0xffffffffU
130 #pragma config BOOTROM_BOOTKEY_0 = 0xffffffffU
131 #pragma config BOOTROM_BOOTKEY_1 = 0xffffffffU
132 #pragma config BOOTROM_BOOTKEY_2 = 0xffffffffU
```

Configuration Options x

Chip Erase Key 0 bits 31:0	0x FFFFFFFF
Chip Erase Key 0 bits 63:32	0x FFFFFFFF
Chip Erase Key 0 bits 95:64	0x FFFFFFFF
Chip Erase Key 0 bits 127:96	0x FFFFFFFF
Chip Erase Key 1 bits 31:0	0x 22222222
Chip Erase Key 1 bits 63:32	0x 22222222
Chip Erase Key 1 bits 95:64	0x 22222222
Chip Erase Key 1 bits 127:96	0x 22222222
Chip Erase Key 2 bits 31:0	0x 22222222
Chip Erase Key 2 bits 63:32	0x 22222222
Chip Erase Key 2 bits 95:64	0x 22222222
Chip Erase Key 2 bits 127:96	0x 22222222
CRC Key bits 31:0	0x FFFFFFFF
CRC Key bits 63:32	0x FFFFFFFF
CRC Key bits 95:64	0x FFFFFFFF
CRC Key bits 127:96	0x FFFFFFFF
Secure Boot Key bits 31:0	0x FFFFFFFF
Secure Boot Key bits 63:32	0x FFFFFFFF