Bit 0 – VDDIO2S VDDIO2 Status

This bit shows the state of the VDDIO2 voltage level.

Writing to this bit has no effect.

Value	Description
0	The VDDIO2 supply voltage is below the acceptable range for operation. The MVIO pins are tri-stated.
1	The VDDIO2 supply voltage is within the acceptable range for operation. The MVIO pin configurations are loaded from the corresponding PORT registers.