

Bit	7	6	5	4	3	2	1	0
	ALWAYSON					REFSEL[2:0]		
Access	R/W					R/W	R/W	R/W
Reset	0					0	0	0

### Bits 2:0 – REFSEL[2:0] Reference Select

This bit field controls the reference voltage level for ADC0.

#### Note:

- The values given for internal references are only typical. Refer to the *Electrical Characteristics* section for further details.

Value	Name	Description
0x0	1V024	Internal 1.024V reference <sup>(1)</sup>
0x1	2V048	Internal 2.048V reference <sup>(1)</sup>
0x2	4V096	Internal 4.096V reference <sup>(1)</sup>
0x3	2V500	Internal 2.500V reference <sup>(1)</sup>
0x4	-	Reserved
0x5	VDD	VDD as reference
0x6	VREFA	External reference from the VREFA pin
0x7	-	Reserved