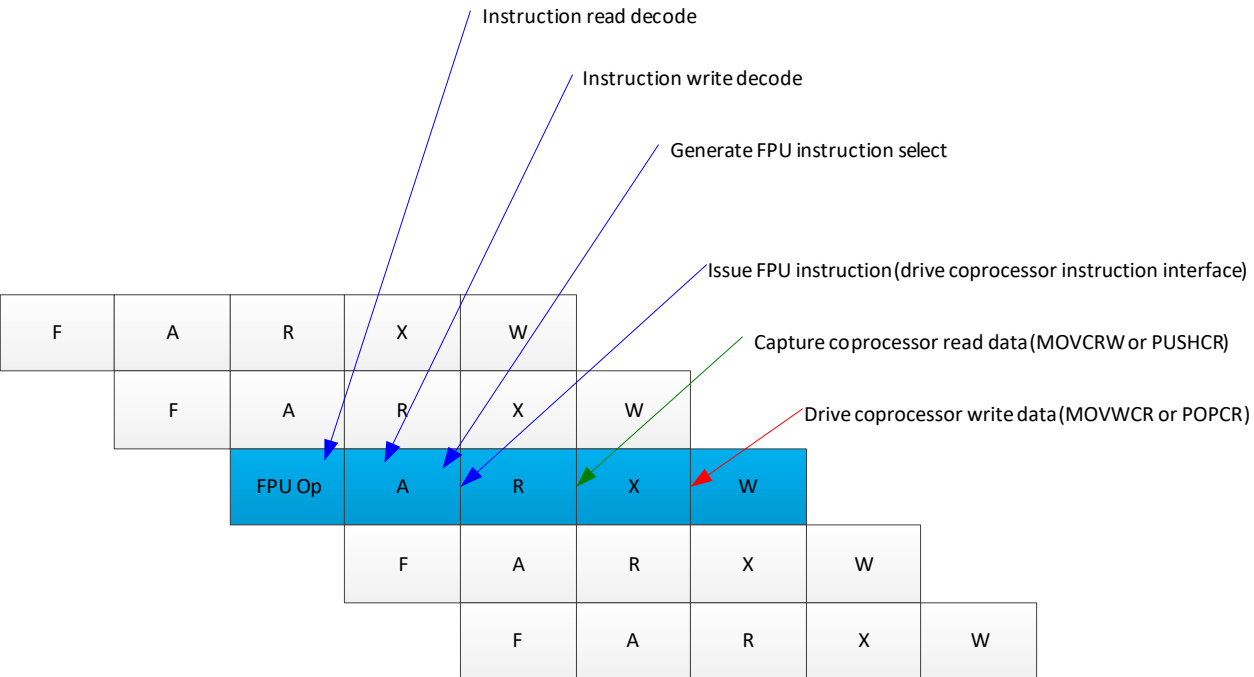




CPU pipeline (only) instruction



CPU-> FPU pipeline instruction