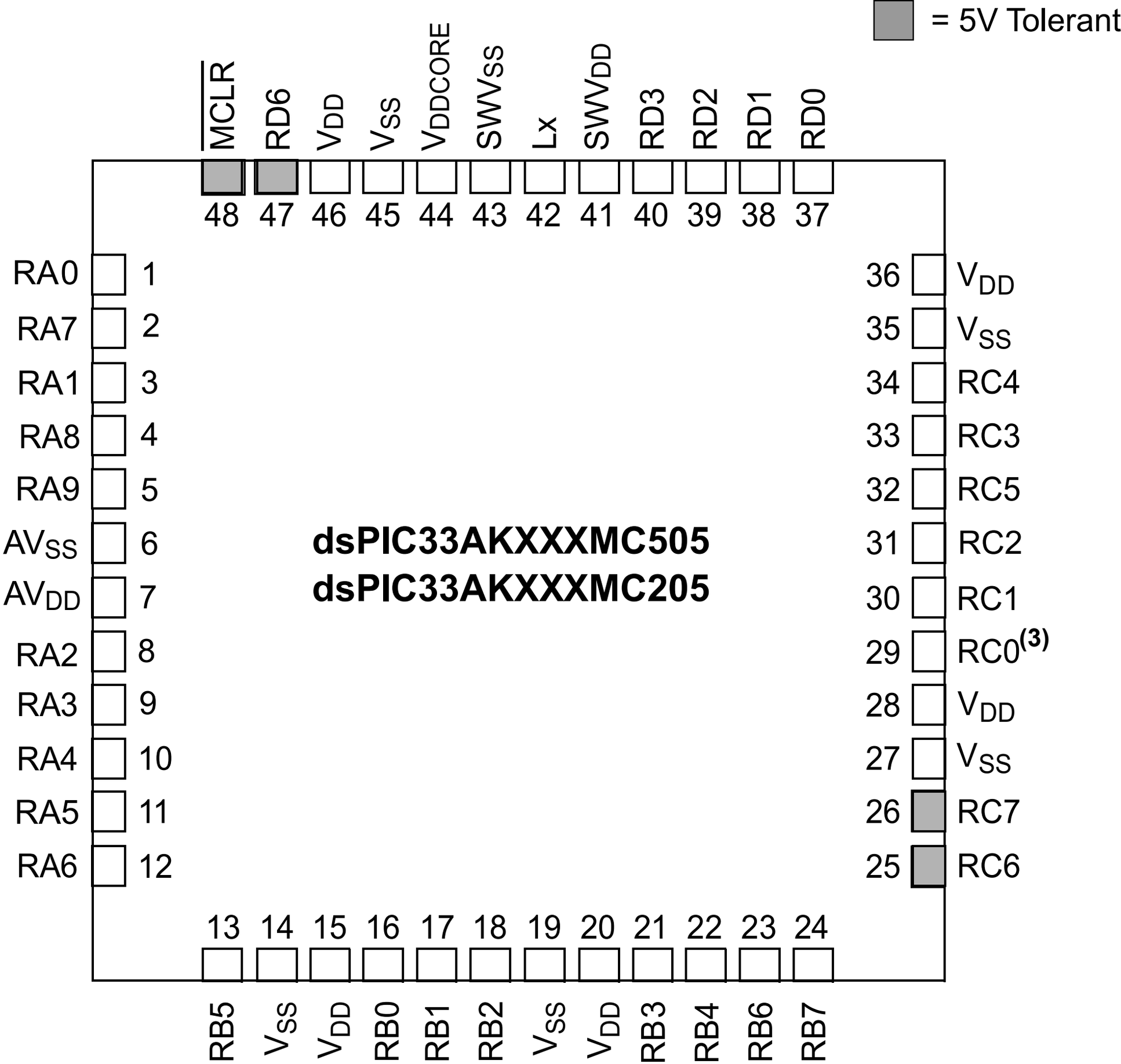
= 5V Tolerant
MCLR
RD6
VDD
VSS
VDDCORE
SWVSS
Lx
SWVDD
RD3
RD2
RD1
RD0
48 47 46 45 44 43 42 41 40 39 38 37
RA0 1
RA7 2
RA1 3
RA8 4
RA9 5
AVSS 6
AVDD 7
RA2 8
RA3 9
RA4 10
RA5 11
RA6 12
36 VDD
35 VSS
34 RC4
33 RC3
32 RC5
31 RC2
30 RC1
29 RC0(3)
28 VDD
27 VSS
26 RC7
25 RC6
dsPIC33AKXXXMC505
dsPIC33AKXXXMC205
13 14 15 16 17 18 19 20 21 22 23 24
RB5
VSS
VDD
RB0
RB1
RB2
VSS
VDD
RB3
RB4
RB6
RB7