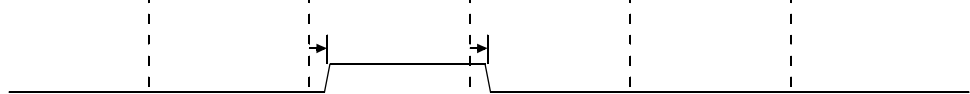


Time-Base Clock



SYNC[4:0] Input



CCPxTMR

