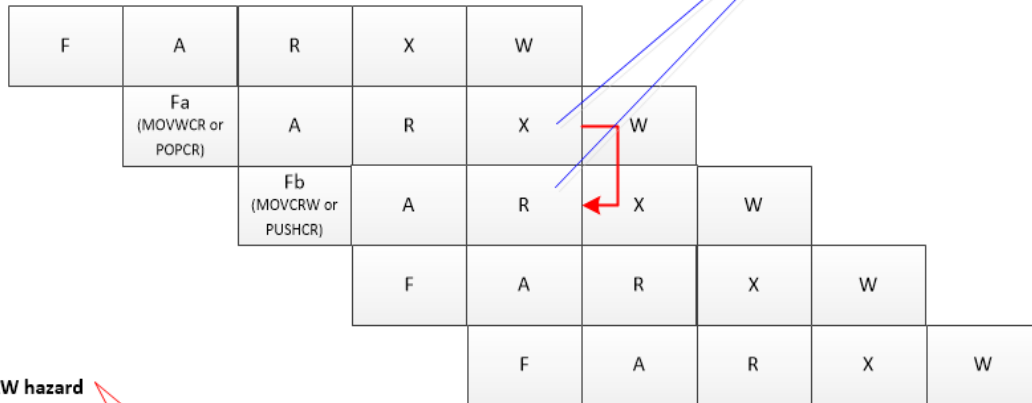


### Register Direct (Correcting **Wreg** Data Value Read):

Read data: Compare Fa dst Wreg to Fb src Wreg

If true, forward Fa dst data to Fb X data-in  
(hazard detected and mitigated in CPU)



RAW hazard

MOV.l F0, W0  
MOV.l W0, F1



CPU pipeline (only) instruction