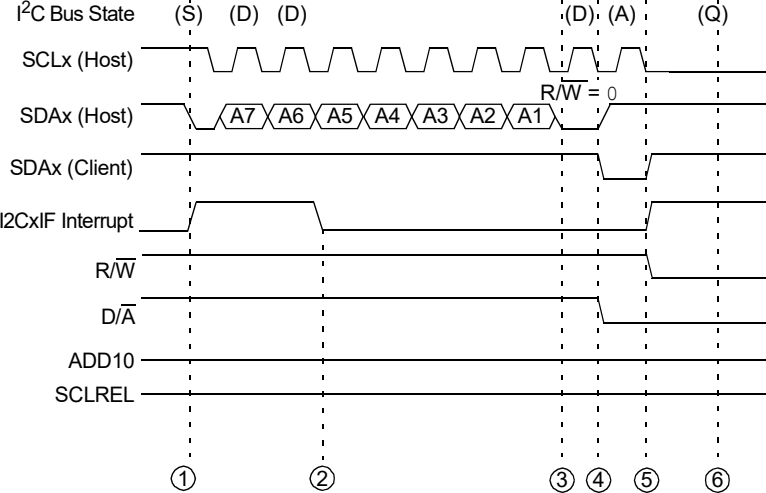




- ① Detecting Start bit enables address detection. If SCIE is set, then the client interrupt is asserted.
- ② User software clears the interrupt flag.
- ③ $R/\overline{W} = 0$ indicates that client receives data bytes.
- ④ Valid address of first byte clears $D/\overline{A}$ status bit. Client generates an $\overline{ACK}$.
- ⑤ $R/\overline{W}$ status bit cleared. Client generates interrupt.
- ⑥ Bus waiting. Client ready to receive data.